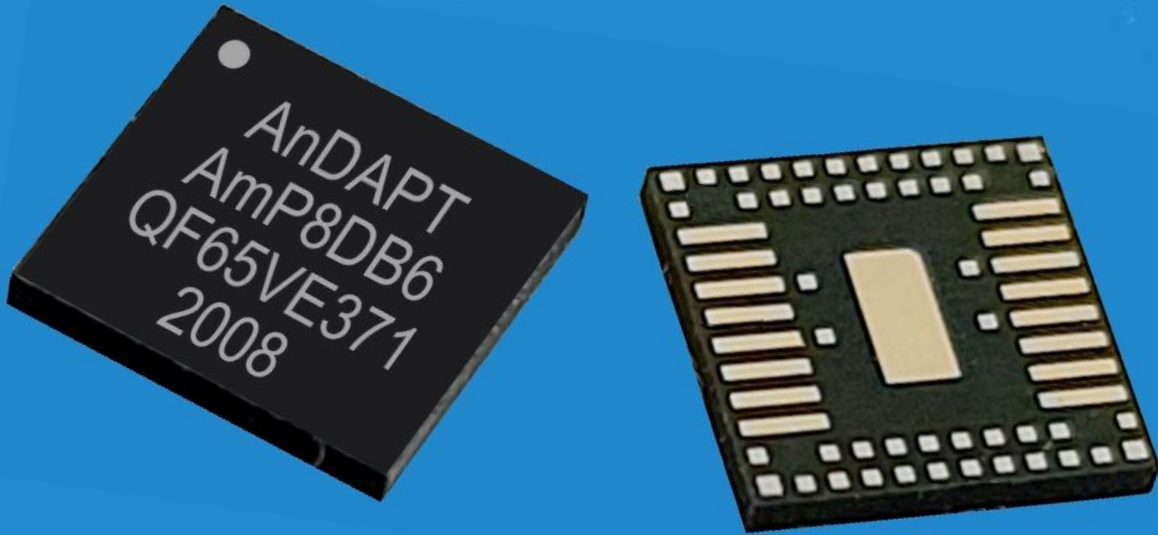


# Kintex-7 (without MGT)

## Mappings & Test Data



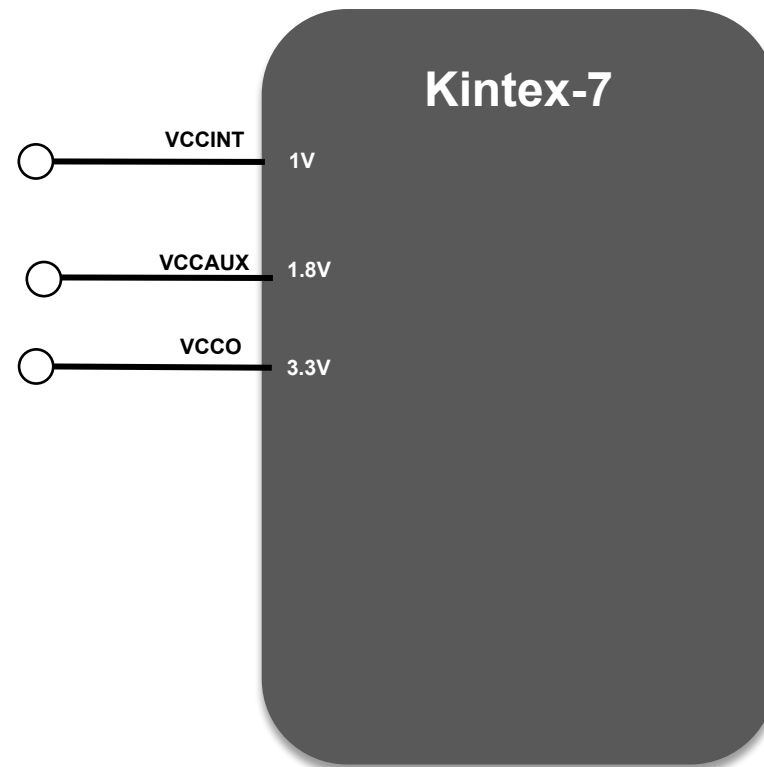
# Contents

- Xilinx Kintex-7 (without MGT) + family of devices SKUs
- Kintex-7 (without MGT) + power maps
- AnDAPT integrated power supply design
- Bench data including efficiency, transients, ripple for each power rail
- AnDAPT PMICs meet or exceed all power performance specs provided by Xilinx for Kintex-7 (without MGT) + family FPGAs

# Kintex-7 (without MGT) Device SKUs Covered

Supported SKUs
XC7K70T
XC7K160T
XC7K325T
XC7K410T
XQ7K410T
XC7K420T
XC7K480T
XC7K355T
XA7K160T
XQ7K70T

# Kintex-7 (without MGT)



# Power Tree: Kintex-7 (without MGT)

PVIN = 12V

#	Rail	Seq	Vout (V)	Iout (A)
1	VCCINT	1	1	2-9
2	VCCAUX	2	1.8	0.3
3	VCCO	3	1.5	1.5

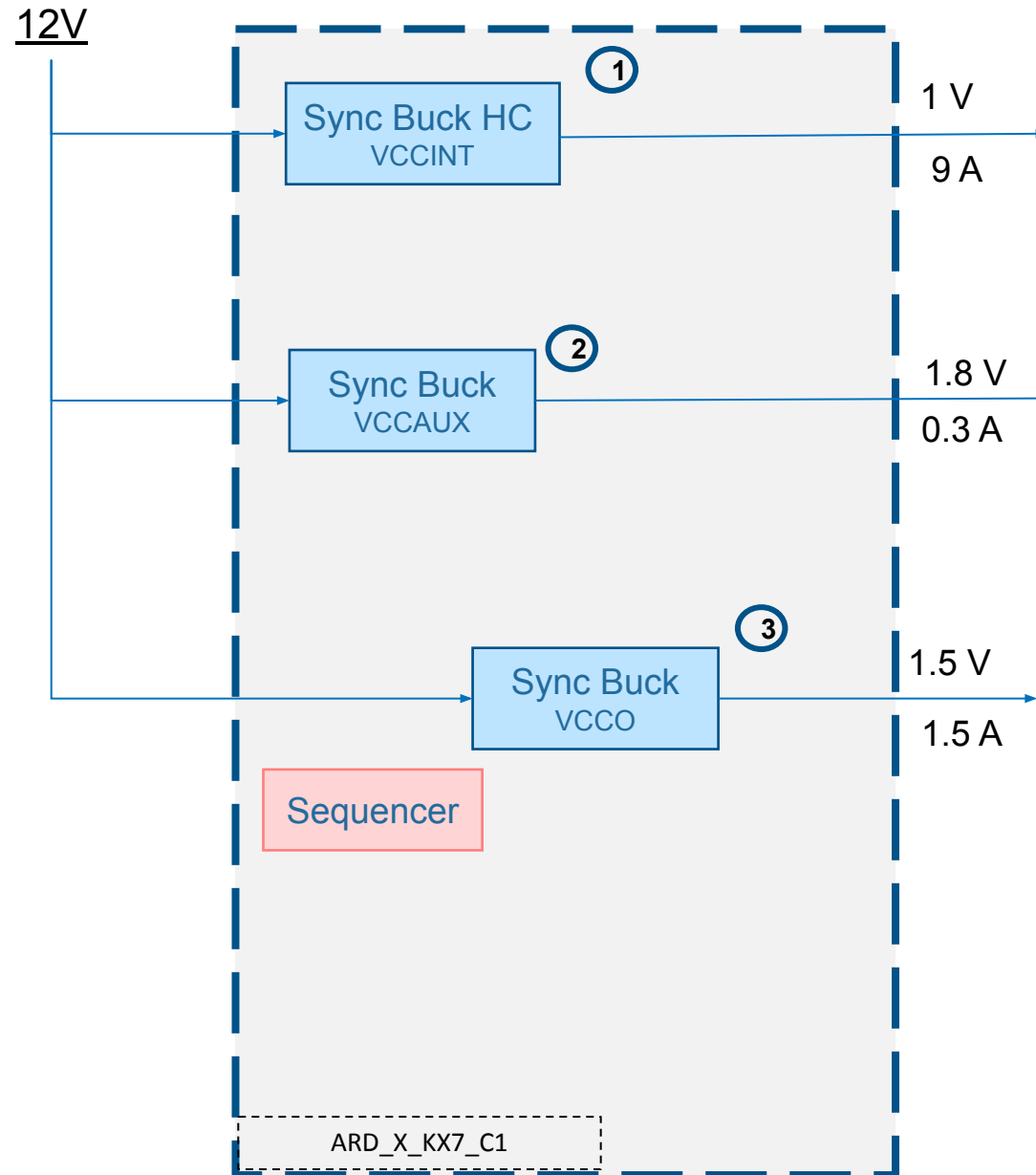
# Power Tree Mapping: Kintex-7 (without MGT)

PVIN = 12V

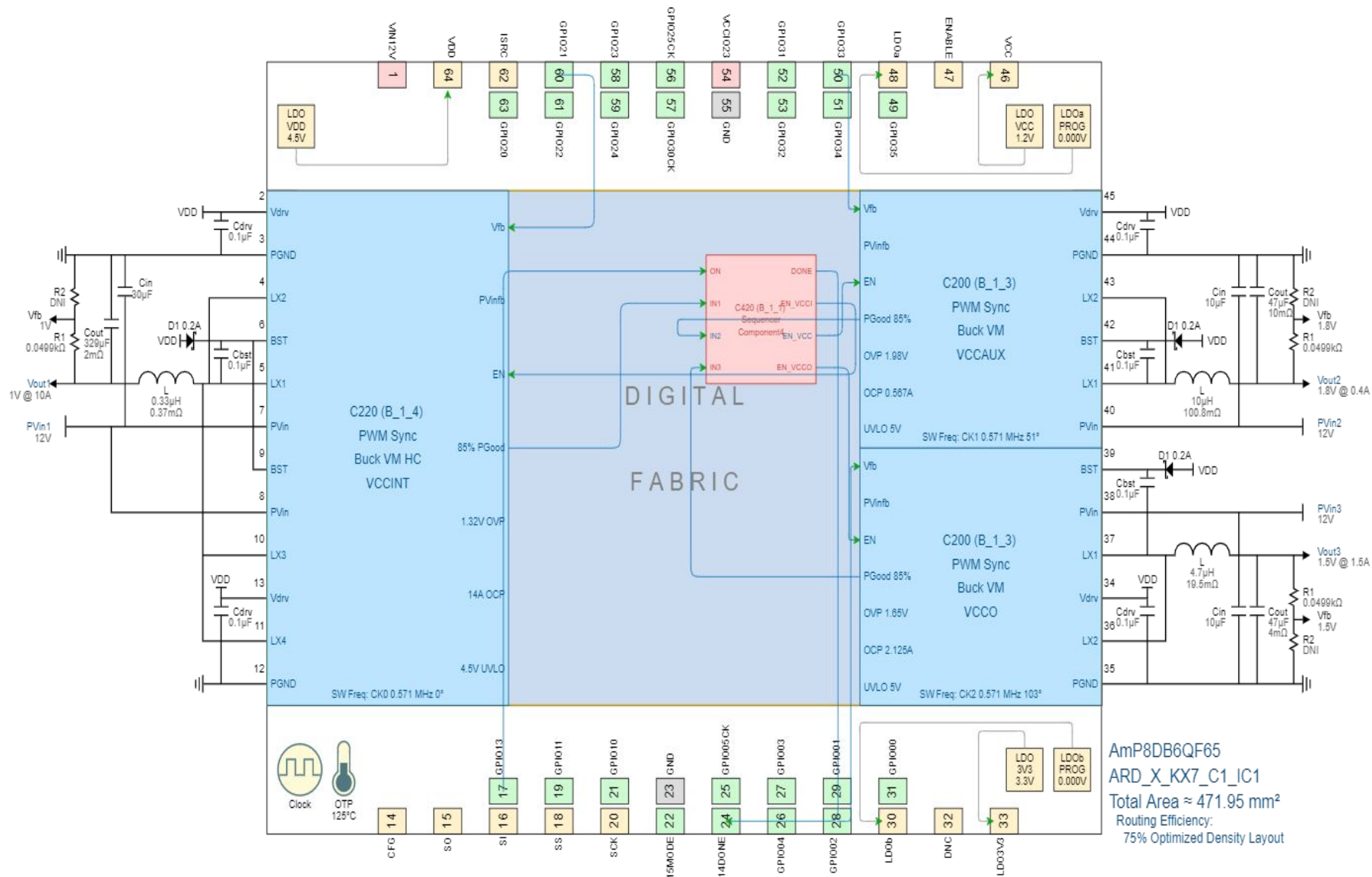
#	Rail	Seq	Power Component	Type	Upstream Rail	Vinput (V)	<u>Vout</u> (V)	<u>Iout</u> (A)	IC
1	VCCINT, BRAM	1	C220	Sync Buck HC	PVIN	12	1	2 to 9	ARD_X_KX7_C1_IC1
2	VCCAUX	2	C200	Sync Buck	PVIN	12	1.8	0.3	ARD_X_KX7_C1_IC1
3	VCCO	3	C200	Sync Buck	PVIN	12	1.5	1.5	ARD_X_KX7_C1_IC1

Estimated total area estimated = 471.95 mm<sup>2</sup>

# Power Tree Mapping

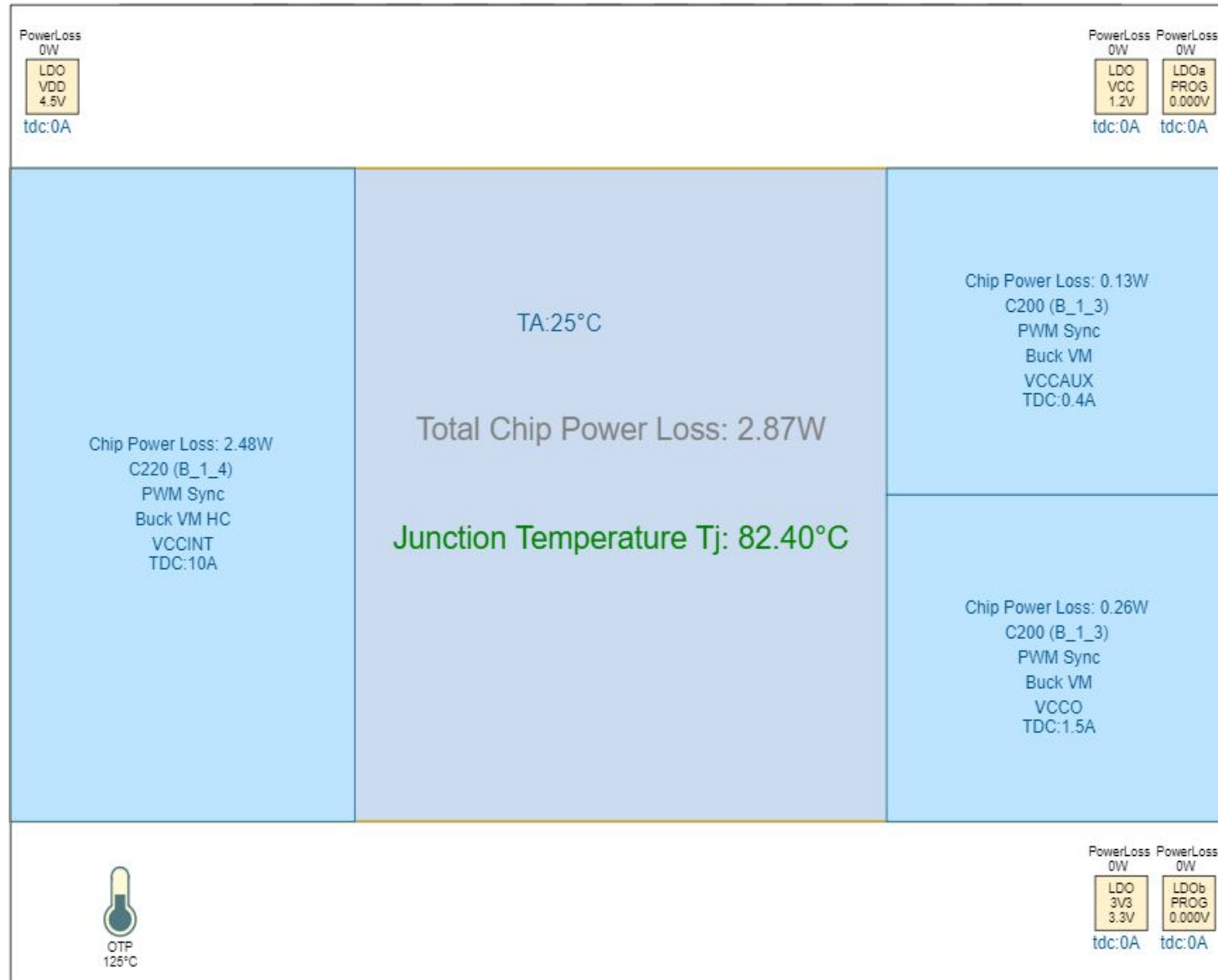


# Mapping (WebAmP View)

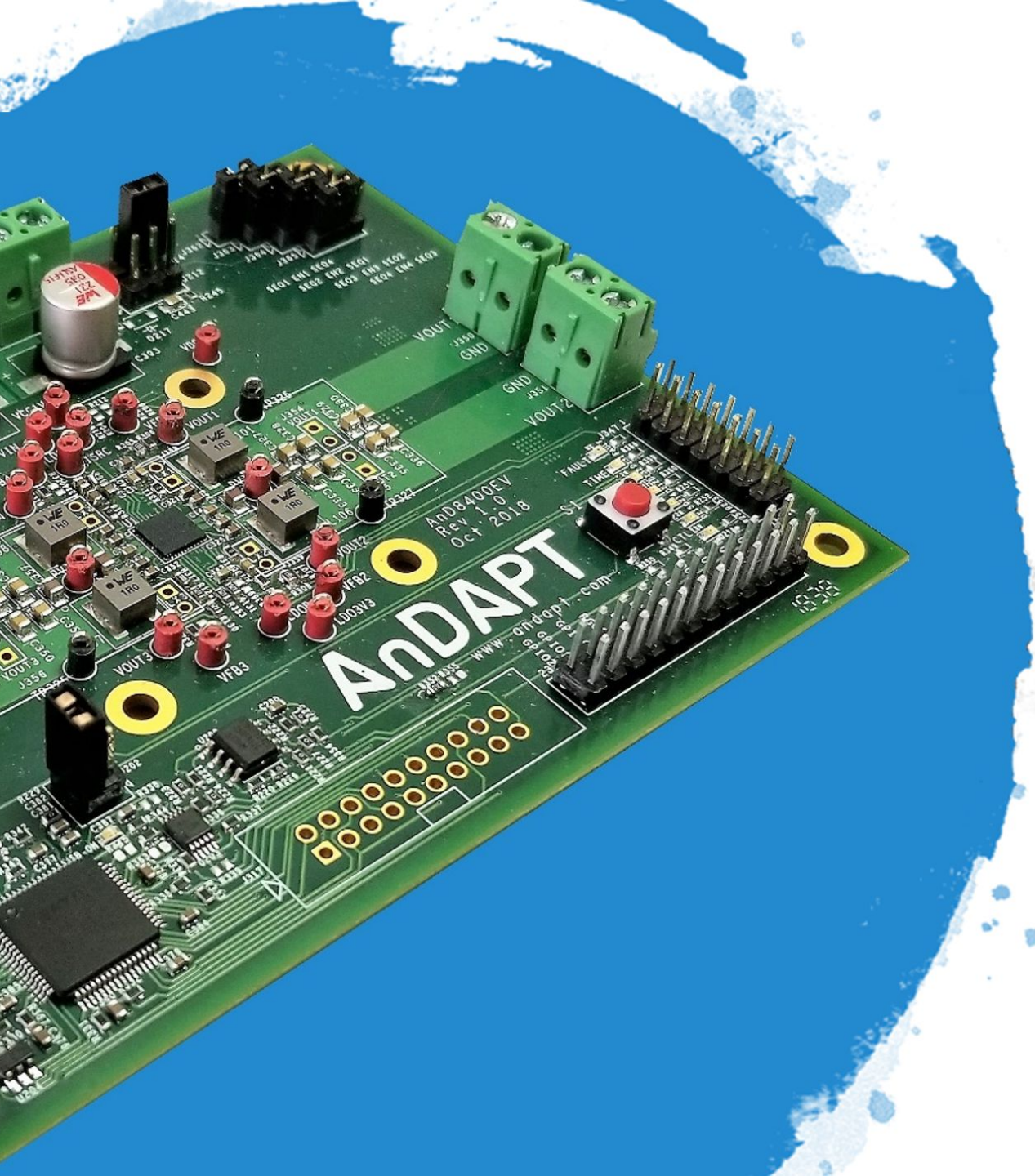




# Mapping (Thermal View)



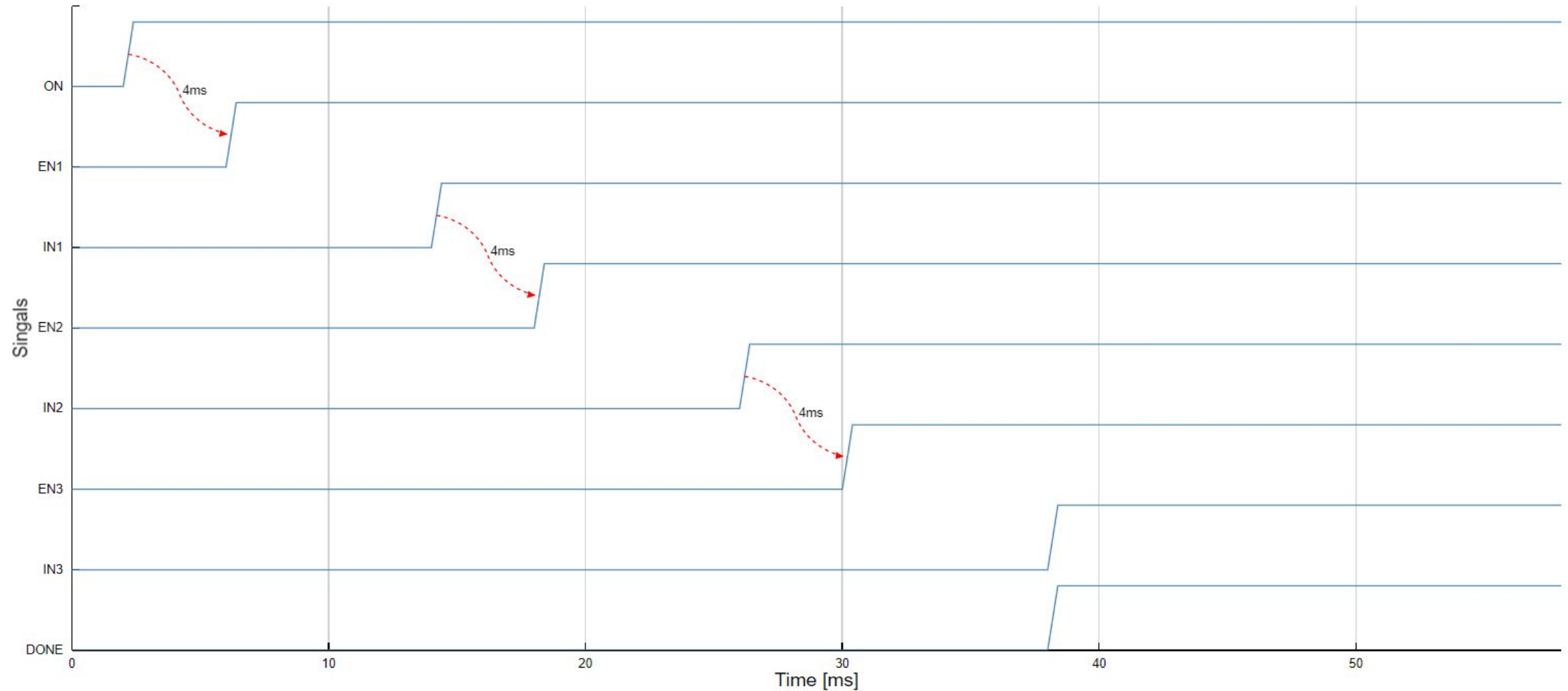
AmP8DB6QF65  
ARD\_X\_KX7\_C1\_IC1  
Total Area ≈ 471.95 mm<sup>2</sup>  
Routing Efficiency:  
75% Optimized Density Layout



# Test Data

**Kintex-7 (without MGT)**

# Integrated Sequencer Graphic (Turn ON)

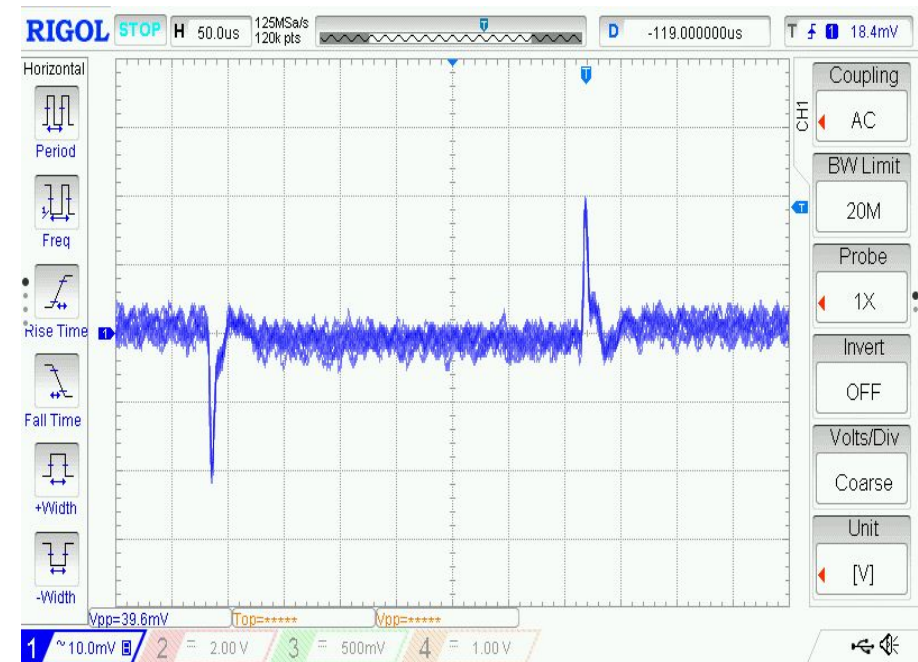
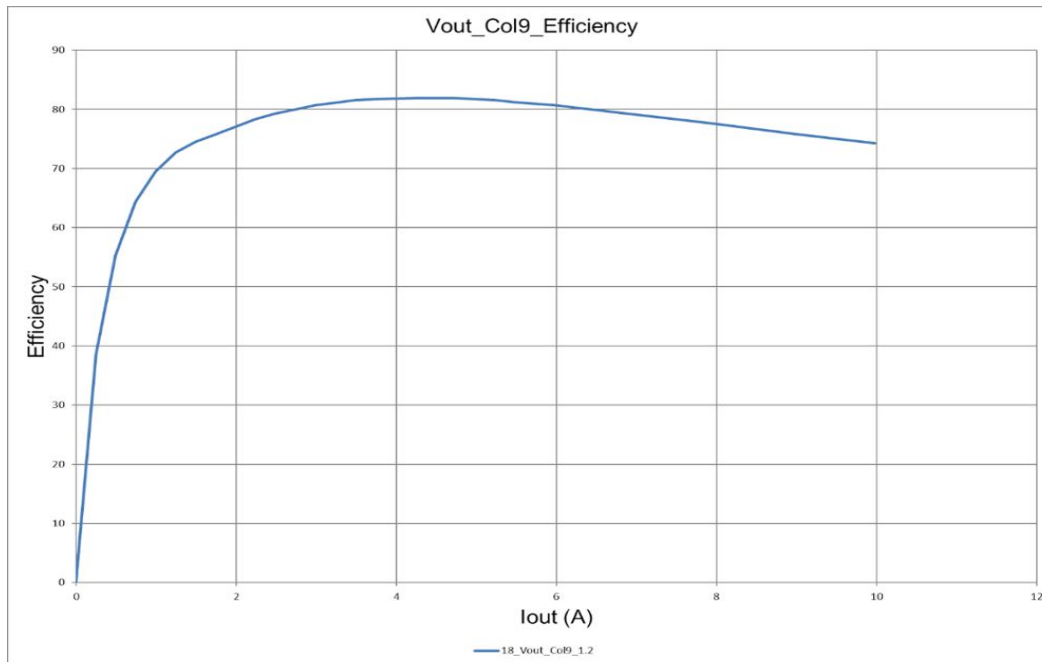


# VCCINT

## 1 V / 9 A

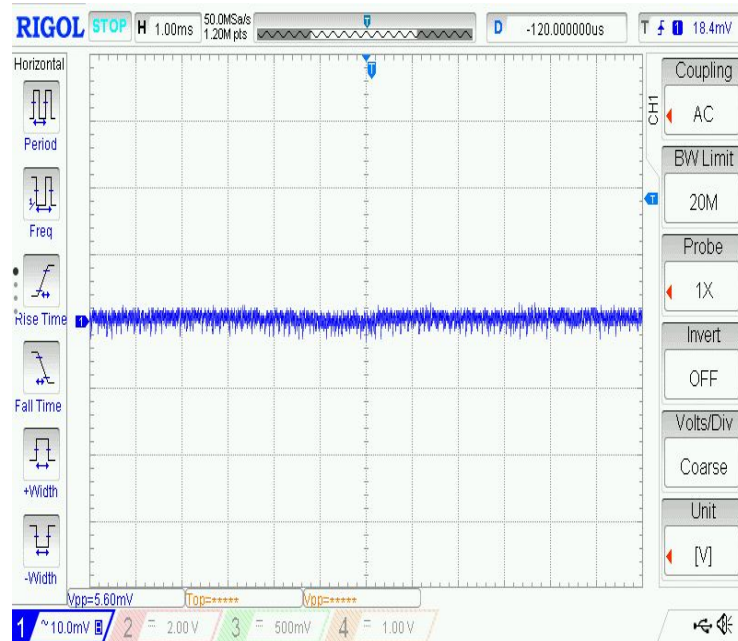
- C220
- $F_{sw} = 571 \text{ kHz}$
- $L = 0.33 \text{ } \mu\text{H}$ , P/N Wurth 744308033
- $C = 7 \times 47 \text{ } \mu\text{F}$

# Efficiency & Transient

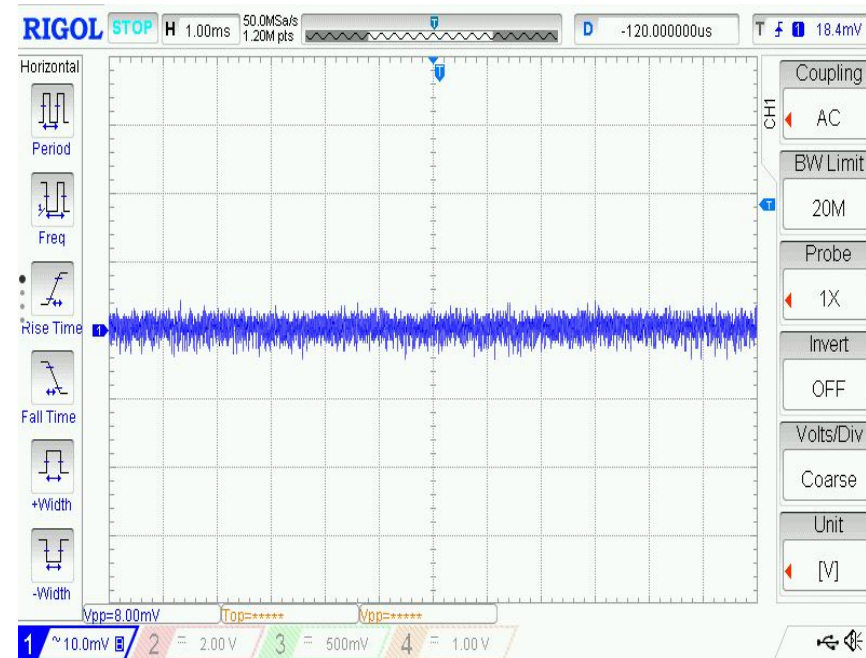


Vout = 1V  
Transient 7.5A – 10A @ 10 A/ $\mu$ s  
 $V_{pp}$  = 39.6 mV  
Fsw = 571 kHz  
Lout = 0.33  $\mu$ H, Cout = 7 x 47  $\mu$ F

# Ripple



No Load  
 $V_{PP} = 5.6 \text{ mV}$



$V_{out} = 1 \text{ V}$

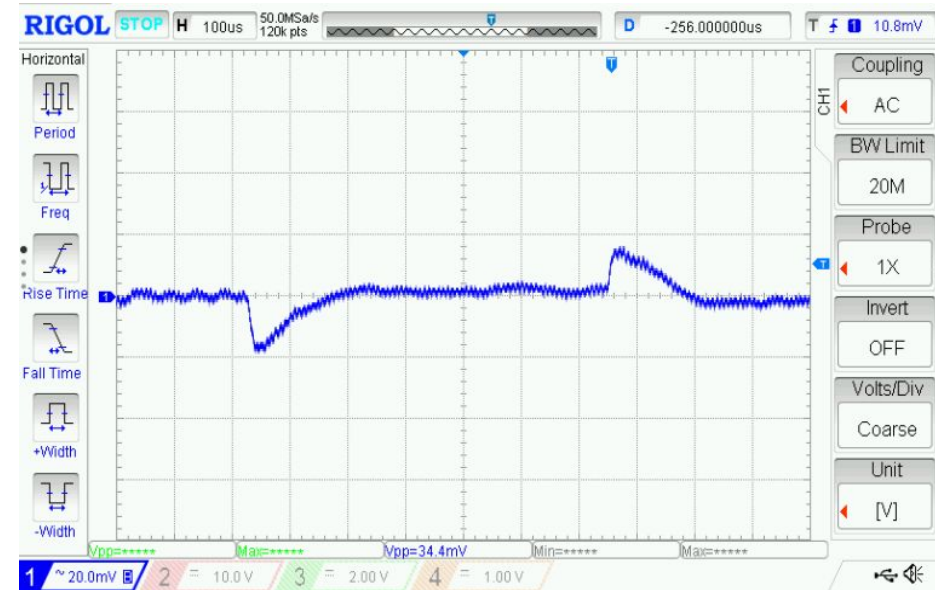
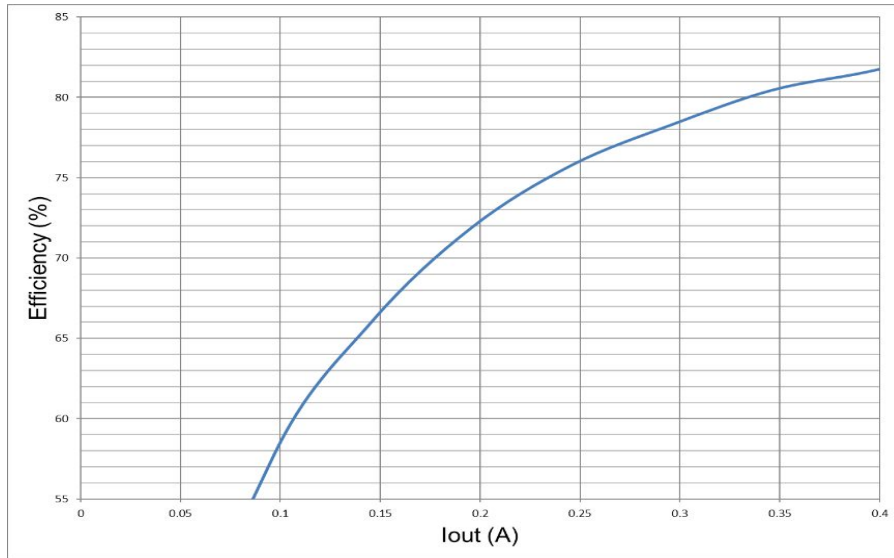
10 A Load  
 $V_{PP} = 8 \text{ mV}$

# VCCAUX

## 1.8 V / 0.3 A

- C200 Sync Buck
- $F_{sw} = 571 \text{ kHz}$
- $L = 10 \mu\text{H}$ , P/N Wurth 74438357100
- $C = 1 \times 47 \mu\text{F}$

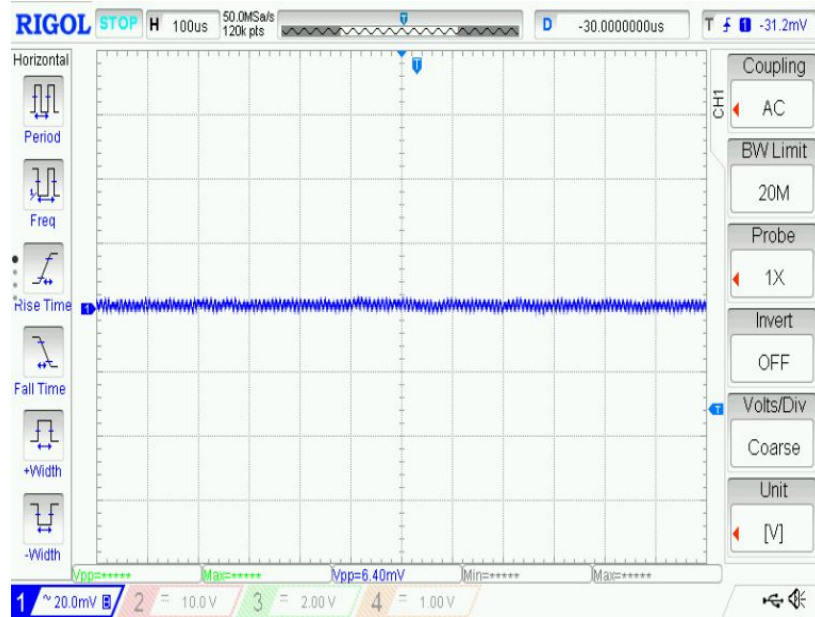
# Efficiency & Transient



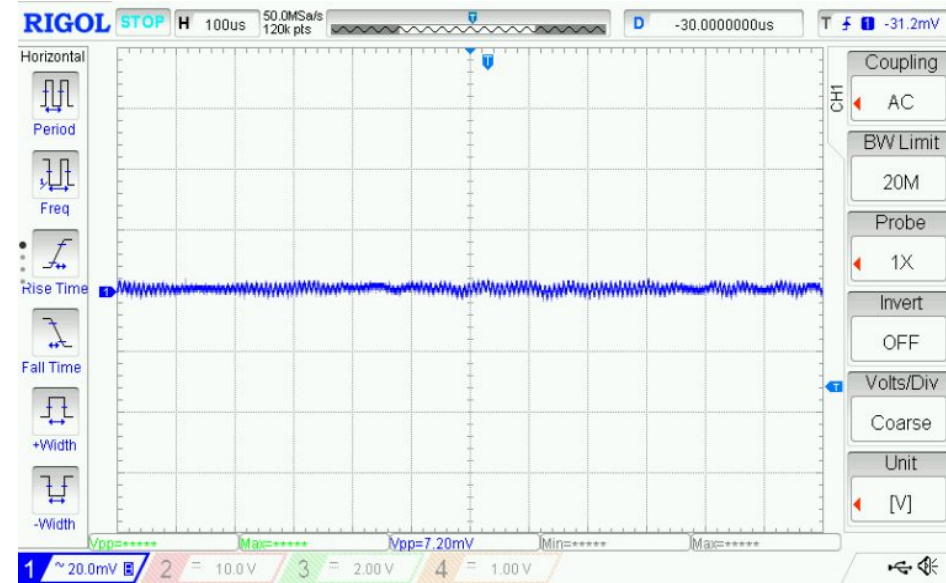
Vout = 1.8V  
Transient 0.12A to 0.4A@2.5 A/us  
Vpp = 34.4 mV  
L = 10 uH and C = 47 uF.  
f = 571 kHz



# Ripple



No Load  
 $V_{PP} = 6.4 \text{ mV}$



$V_{out} = 1.8 \text{ V}$

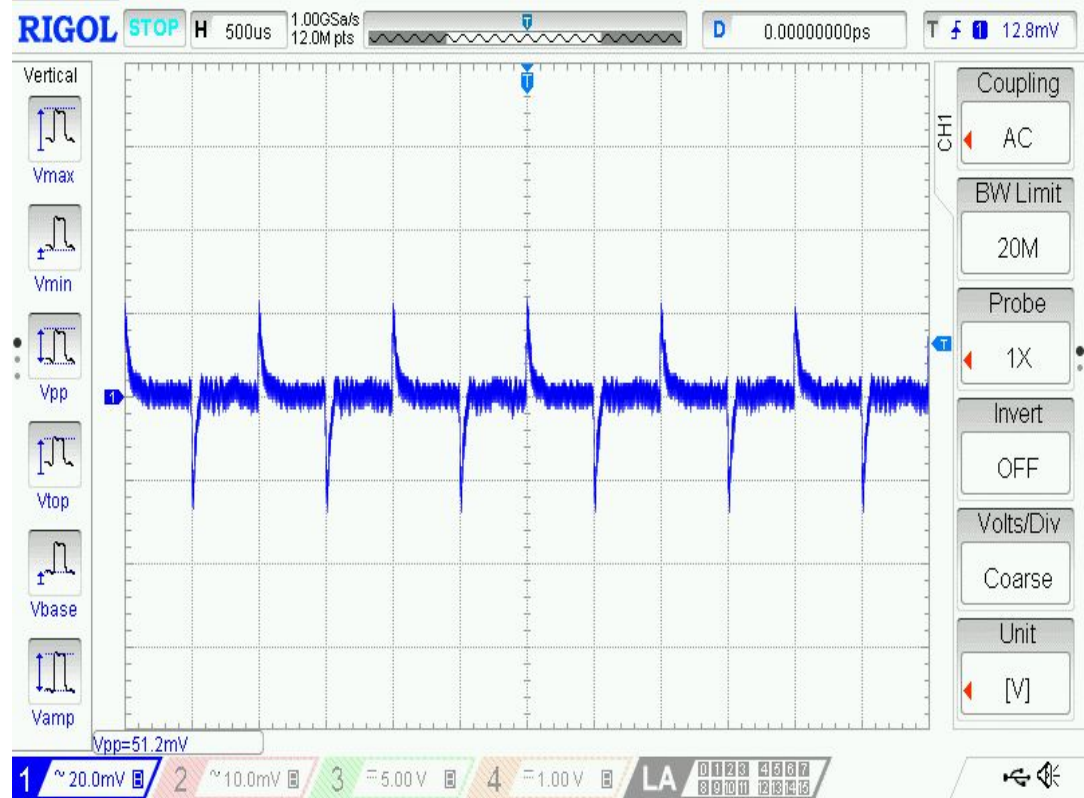
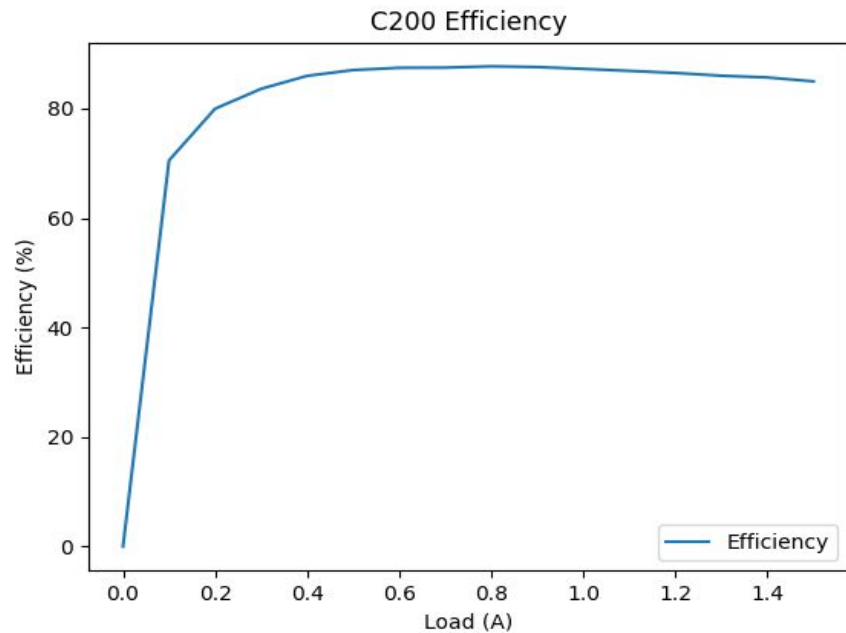
0.4A Load  
 $V_{PP} = 7.2 \text{ mV}$

# VCCO

## 1.5 V / 1.5 A

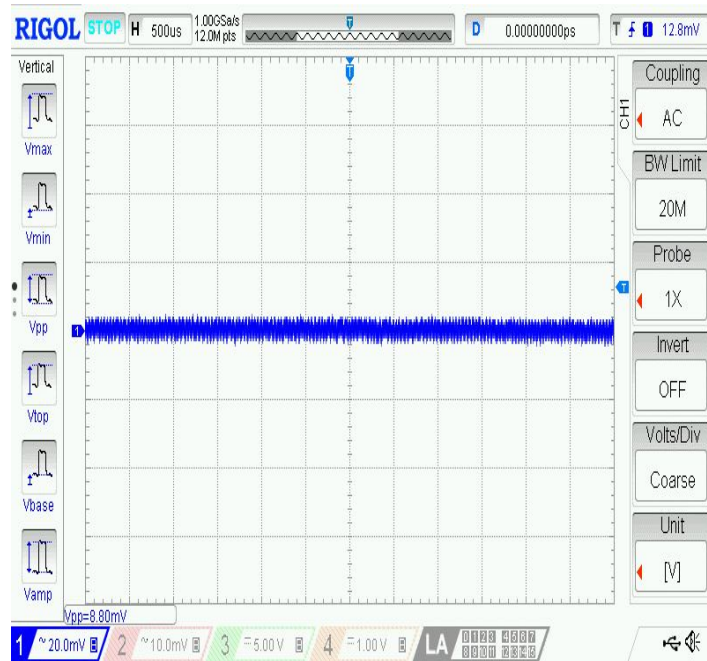
- C200 Sync Buck
- $F_{sw} = 571 \text{ kHz}$
- $L = 4.7 \text{ } \mu\text{H}$ , P/N Wurth 744311470
- $C = 1 \times 47 \text{ } \mu\text{F}$

# Efficiency & Transient

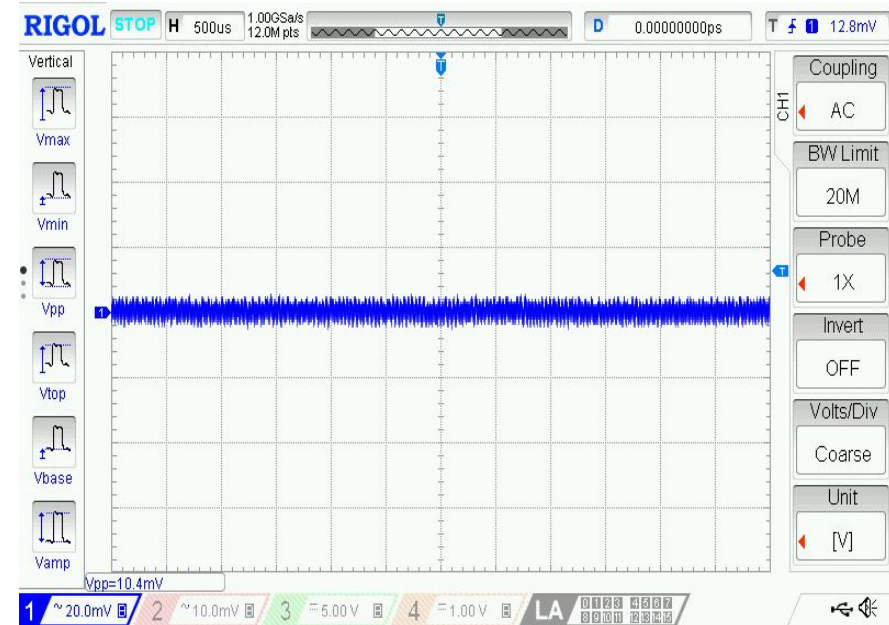


Vout = 1.5V  
Transient 0.15A to 1.5A@10 A/us  
Vpp = 51.2 mV  
L = 4.7 uH and C = 47 uF.  
f = 571 kHz

# Ripple



No Load  
 $V_{PP} = 8.80 \text{ mV}$



$V_{out} = 1.5 \text{ V}$

1.5 A Load  
 $V_{PP} = 10.4 \text{ mV}$



**Thank You**