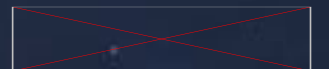


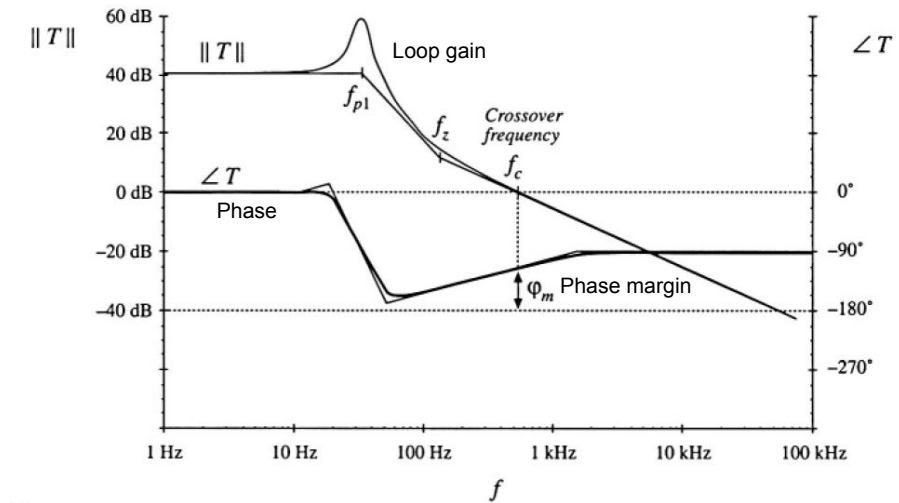
C200/C220 Compensator Design: Voltage Mode Control

Sep 2nd 2022



Bode Plot Basics

- Why compensator?
 - In a Voltage regulator, the output filter (Lout, Cout) and the PWM modulator create the converter forward plant
 - Bode plots (loop gain and phase) are employed to analyze the frequency response of this linear system
 - In the loop, a Compensator is inserted to ensure stability and dynamic performance



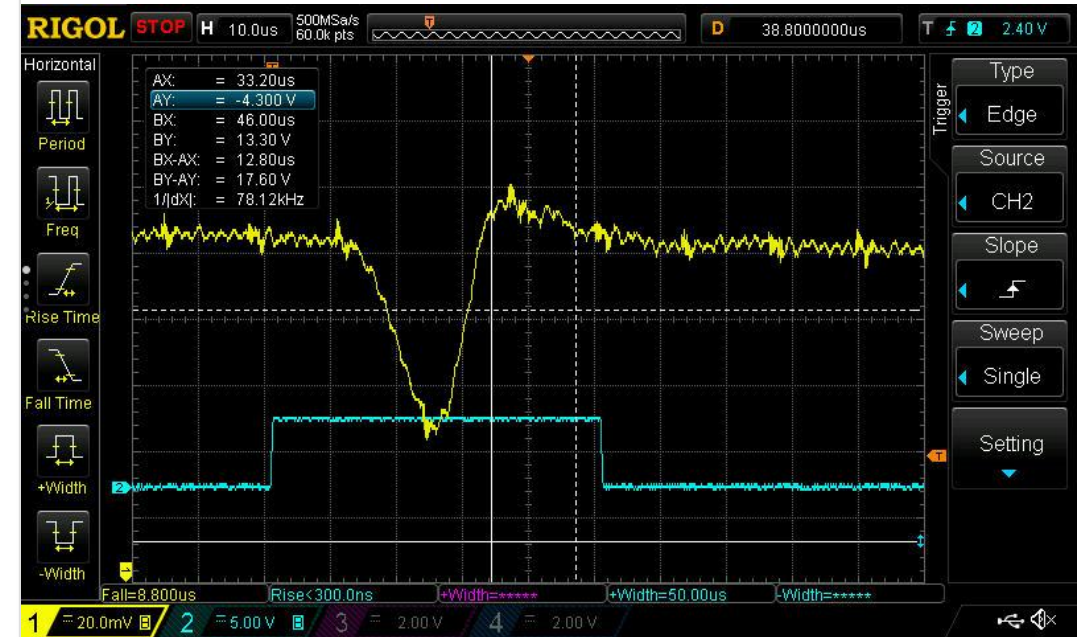
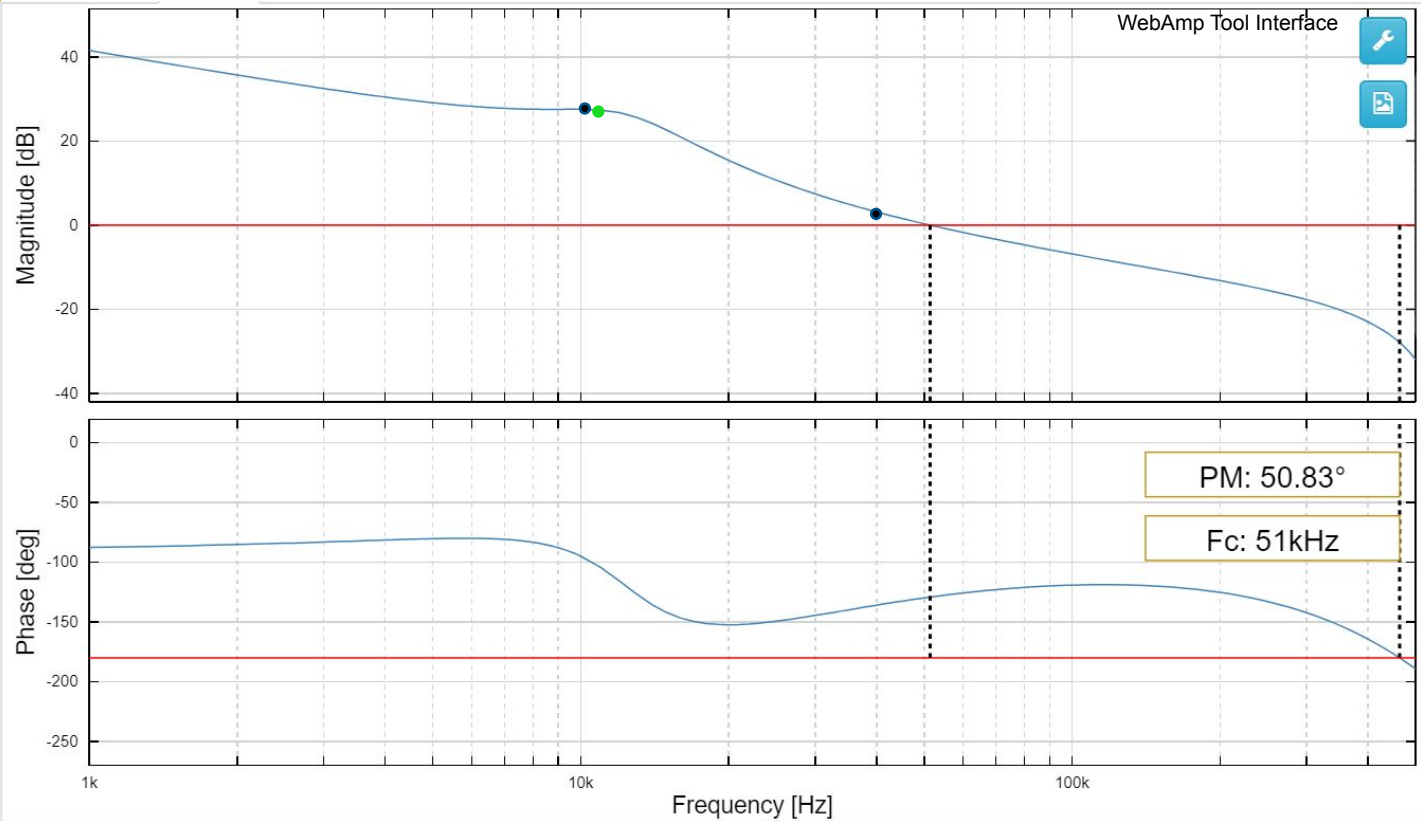
Compensator Design Example

- Power component: C200/C220
- $V_{in} = 12\text{ V}$
- $V_{out} = 1.3\text{ V}$
- $f_{SW} = 571\text{ kHz}$
- $L_{out}, L = 1.1\text{ }\mu\text{H}$
- $DCR = 3.15\text{ m}\Omega$
- $C_{out}, C = 4 \times 47\text{ }\mu\text{F}$
- Resonant frequency $f_{LC} = 1/(2 \times \pi \times \sqrt{LC}) = 11\text{ kHz}$
- Load: 0 – 5 A
- Voltage mode compensator parameters: f_{z1} , f_{z2} , DC Gain



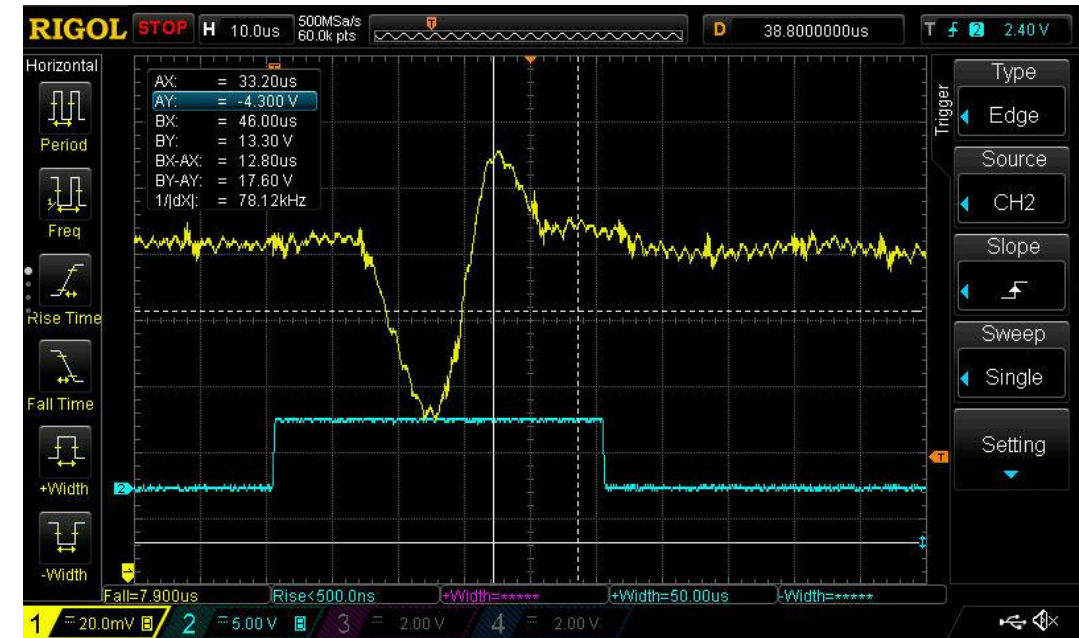
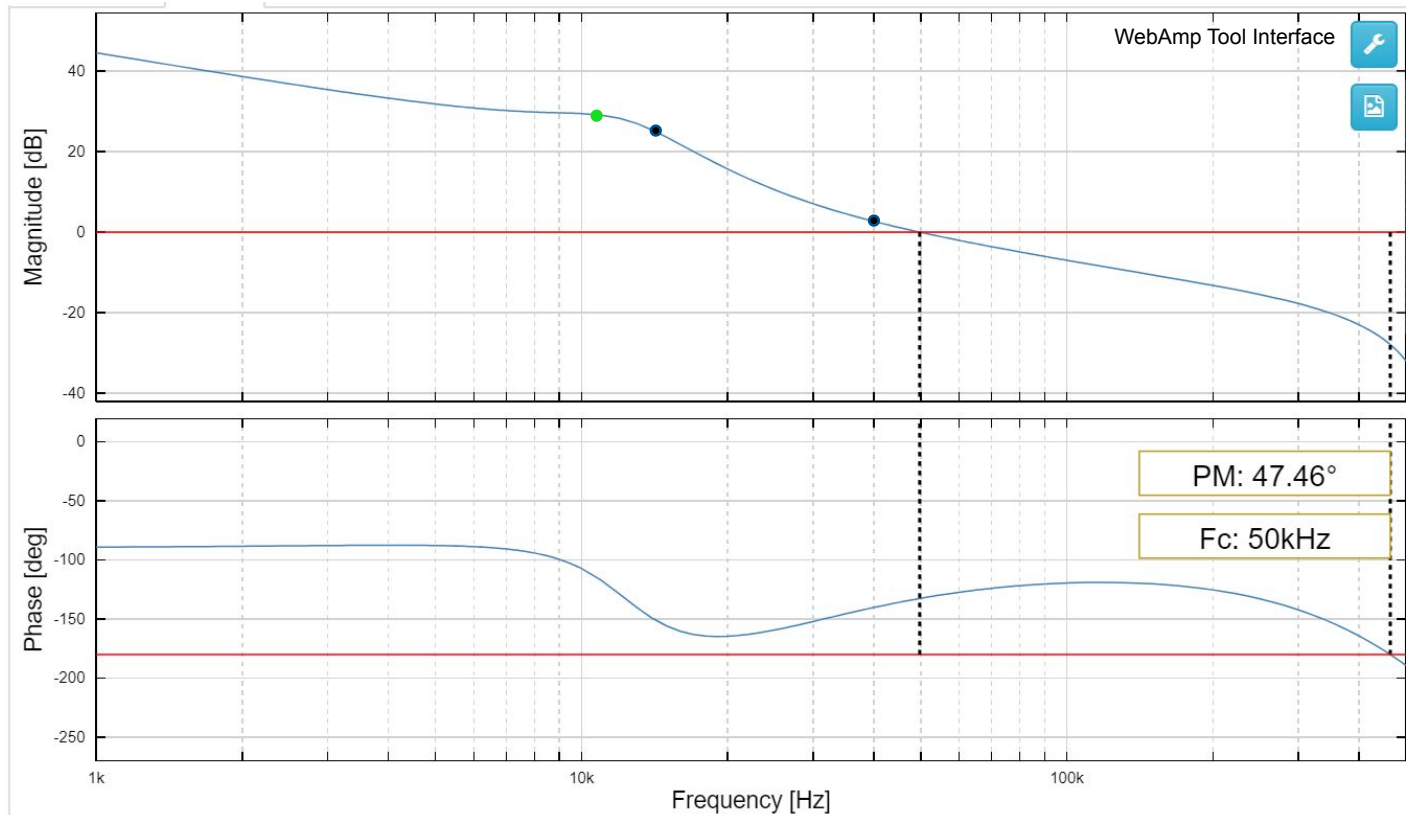
Compensator Design 1

Gain = 650
Z1 = 10.6 kHz
Z2 = 40 kHz



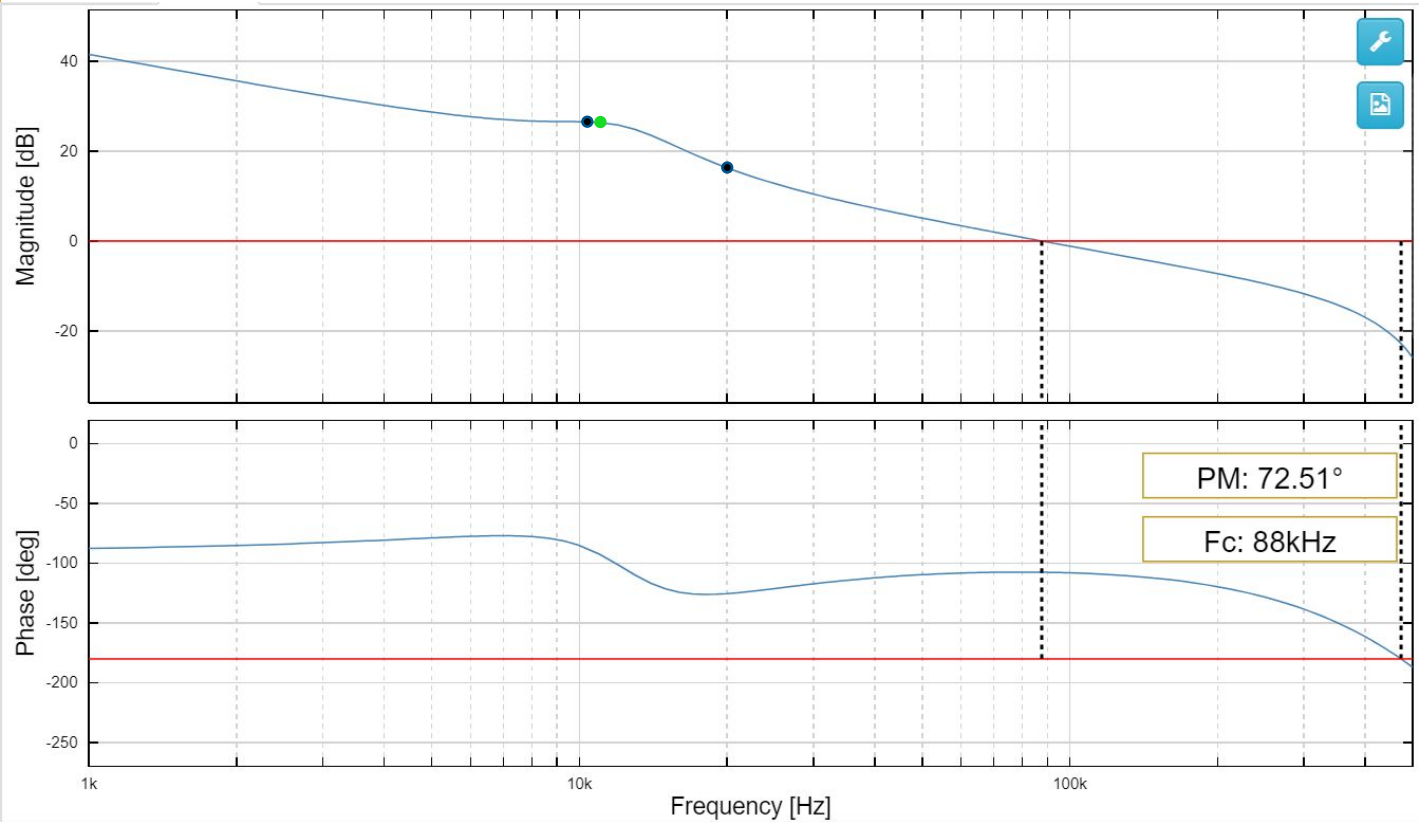
Compensator Design 2 - Increasing f_{z1}

Gain = 650
Z1 = 15 kHz
Z2 = 40 kHz



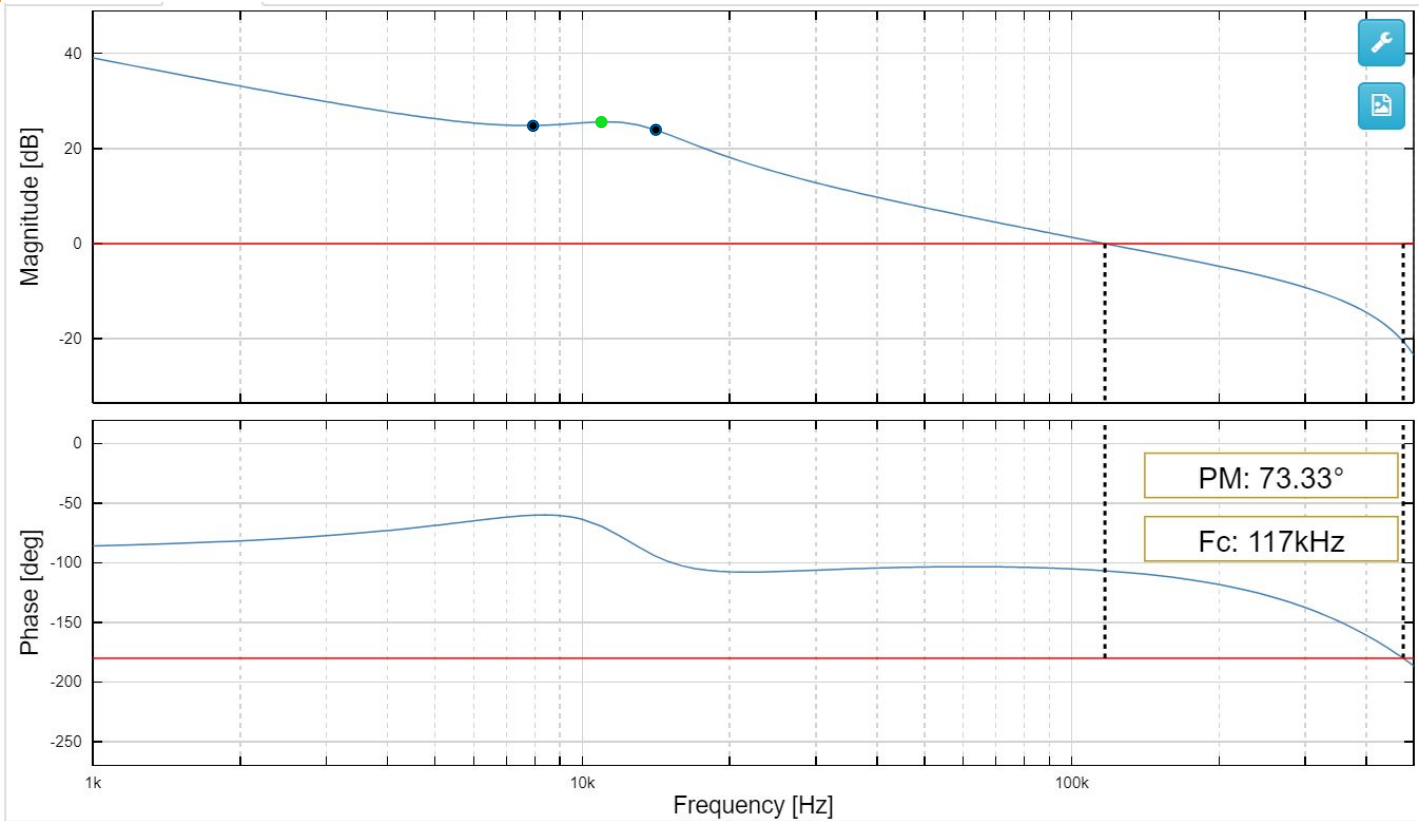
Compensator Design 3 - Decreasing f_{Z2}

Gain = 650
Z1 = 10.6 kHz
Z2 = 20 kHz



Compensator Design 4 - Decreasing f_{z1} and f_{z2}

Gain = 650
Z1 = 8 kHz
Z2 = 15 kHz



- Crossover frequency, $f_c = 117$ kHz is too high for $F_{sw} = 571$ kHz, resulting in ringing and possible instability
- Thumb-rule: choose $f_c \leq F_{sw}/10$
- Bode plot is based on linear modeling, PCB parasitics and other non-linearities affect Bode plot accuracy beyond frequencies $= F_{sw}/5$

Effects of different Gain settings

Z1 = 10.6 kHz

Z2 = 40 kHz

Gain	PM (deg)	Fc (kHz)
650	51	51
800	54	60
950	57	70

- Increased DC Gain increases crossover frequency, f_c



Compensator Design Steps

1. Place first zero, f_{z1} , slightly below filter's resonant frequency, $f_{LC} = 1/(2*\pi*\sqrt{LC})$
2. Place second zero, f_{z2} at around $f_{sw}/12$
3. Modify DC gain to obtain desired crossover frequency

- Recommendations:

- Crossover frequency (F_c):

- Optimal: $0.05*f_{sw} < F_c < 0.1*f_{sw}$
 - Acceptable: $0.03*f_{sw} < F_c < 0.12*f_{sw}$

- Phase Margin (PM):

- Optimal: $PM > 45^\circ$
 - Acceptable: $PM > 35^\circ$

