

AnDAPT: Getting Started

The screenshot shows the AnDAPT website homepage. The browser address bar displays 'andapt.com'. The navigation menu includes 'Adaptable PMIC', 'On-Demand PMIC', 'Web Tools', 'Power Components', 'Documents', 'Shop', 'Register', 'Login', and '0 items'. An orange arrow points from the 'Register' link to a text box that says 'Register to access design tools/documentations etc'. The main content area features a laptop displaying the 'AmP Platform' with a grid of options: 'PWM BOOST', 'PWM BUCK', and 'LDO SSR'. Below the laptop is a 3D rendering of an AnDAPT chip. To the right, a large heading reads 'Announcing release of ADAPTABLE PMIC', followed by a sub-heading: 'AnDAPT introduces first family of PMICs integrating DrMOS Controller along with multiple Buck regulators for unprecedented flexibility'. Below this is an orange 'PRESS RELEASE' button. At the bottom, a teal banner contains a quote: 'Architecture flexibility and programmability allows us to use AnDAPT in multiple applications.' attributed to '-POWER DESIGN MANAGER, ANDAPT CUSTOMER'.

WebAmp Access

Web Tools page gives access to:

- WebAdapter
- WebAmp
- How to videos, guides and more

Design - Andapt x +
andapt.com/?page_id=174
Apps AnDAPT R News Py

AnDAPT™ Adaptable PMIC On-Demand PMIC **Web Tools** Power Components Documents Shop Logout 0 items

Home > Design

Adaptable PMIC Tool

WebAdapter™ Tools are used to configure Adaptable PMIC solutions. Set the output voltages and sequencing of your design with resistors or by using the WebAdapter tool. Interface with Adaptable PMIC Evaluation Boards to program external FLASH or download to the Adaptable PMIC on your board.

[WEBADAPTER](#) [WEBADAPTER DATA BRIEF](#)

On-Demand PMIC Tool

WebAmp™ Development Tools are used to design and configure On-Demand custom PMIC solutions. Drag-and-Drop Power Components, compile and download your design. Interface with AmP Platform Demonstration Board to program external FLASH or download to AmP Platform.

[WEBAMP](#) [WEBAMP DATA BRIEF](#) [STARTING INSTRUCTIONS](#)

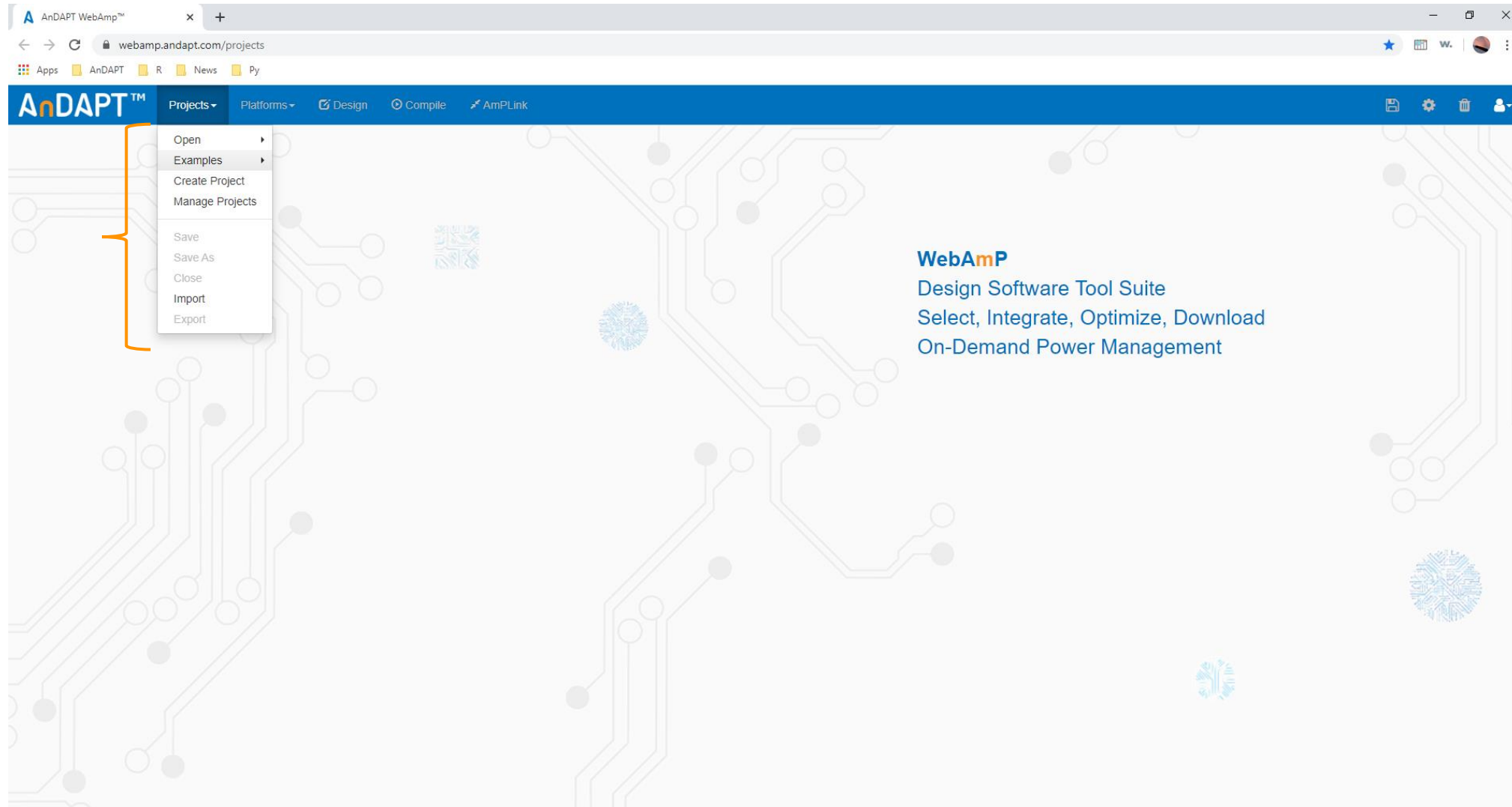
WebAmp Development Software

To create on-demand power management devices, AnDAPT provides

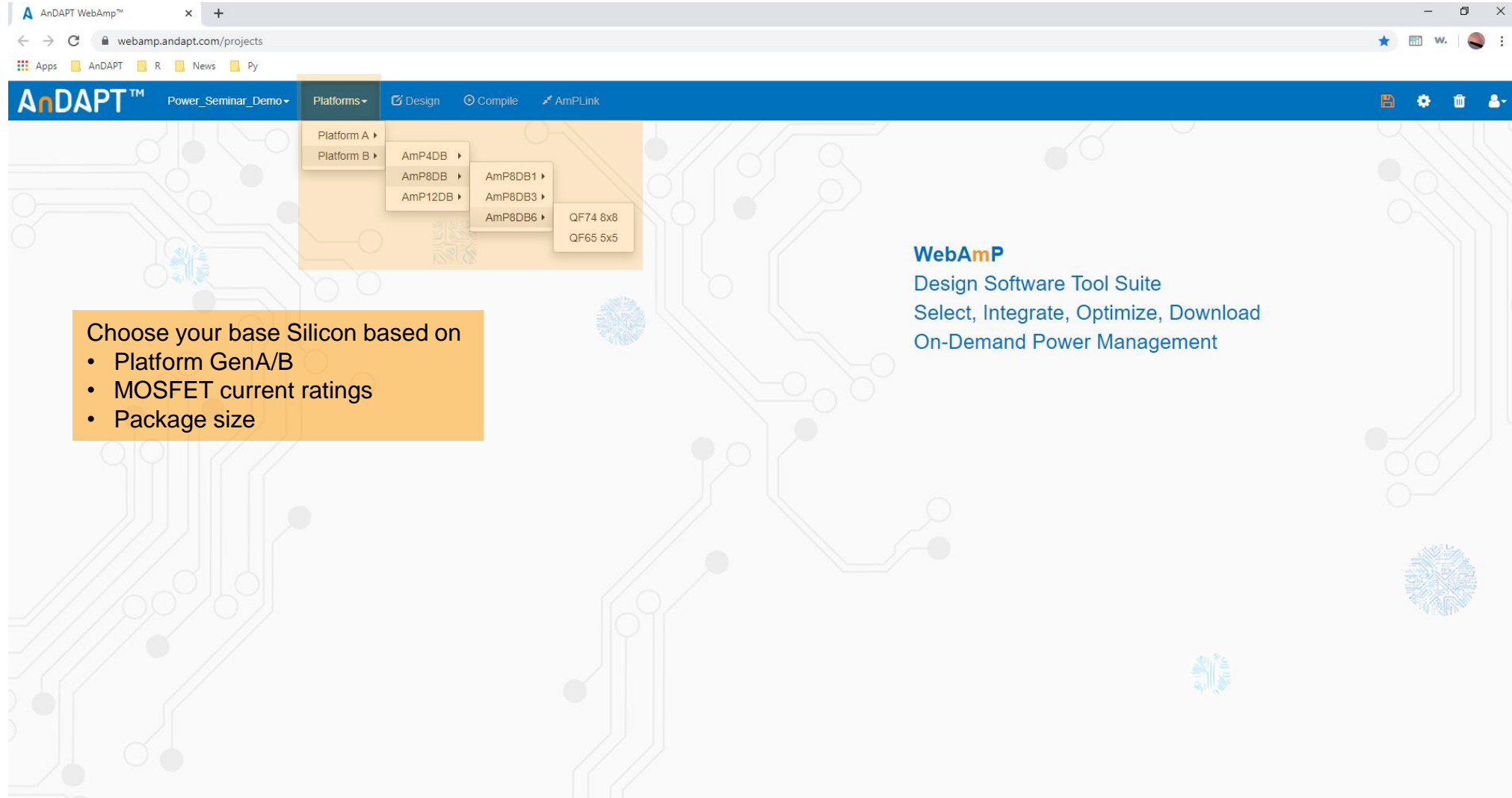
Web Tools page gives access to:

- WebAdapter
- WebAmp
- How to videos, guides and more

WebAmp for On-Demand Power Solution



Choose Base Silicon



The screenshot shows the AnDAPT WebAmP web interface. The browser address bar displays "webamp.andapt.com/projects". The page header includes the AnDAPT logo, a "Power_Seminar_Demo" dropdown, and navigation links for "Platforms", "Design", "Compile", and "AmPLink". A "Platforms" dropdown menu is open, showing a tree structure: Platform A, Platform B, AmP4DB, AmP8DB, AmP12DB, AmP8DB1, AmP8DB3, AmP8DB6, QF74 8x8, and QF65 5x5. An orange callout box on the left contains the text "Choose your base Silicon based on" followed by a bulleted list: Platform GenA/B, MOSFET current ratings, and Package size. On the right side of the interface, the text "WebAmP Design Software Tool Suite Select, Integrate, Optimize, Download On-Demand Power Management" is displayed. The background features a light blue circuit board pattern.

Choose your base Silicon based on

- Platform GenA/B
- MOSFET current ratings
- Package size

WebAmP
Design Software Tool Suite
Select, Integrate, Optimize, Download
On-Demand Power Management

Design Your IC

Choose Components

- ▼ PWM Switching Regulator
 - ▼ PWM Async Buck CM
 - C150_B_1_0
 - ▼ PWM Sync Buck VM
 - C200_B_1_0
 - I200_B_1_0 I2C Control
 - ▼ PWM Sync Buck VM HC
 - C220_B_1_0
- ▼ Linear Regulator
 - ▼ LDO-SSR
 - C710_B_1_0
 - C710_B_1_1
 - ▼ Load Switch
 - ▼ Load Switch
 - C750_B_1_0
 - C750_B_1_1
 - ▼ DrMos Controller
 - ▼ PWM SinglePhase DrMOS
 - C860_B_1_0
 - I860_B_1_0 I2C Control
 - I860_B_2_0 I2C Control
- ▼ Supervisor
 - ▼ Sequencer
 - C420_B_1_0
 - C425_B_1_0 Mux
 - ▼ Digital Blocks
 - C430_B_1_0 Gate
 - C431_B_1_0 Clock Source
 - C432_B_1_0 DFF4
 - C434_B_1_0 Reference Comparator
 - ▼ Telemetry Interface
 - I480_B_1_0 I2C Control

Component library contains-

- Switching regulator topologies
- Linear regulators
- Load switches
- DrMOS controllers
- Sequencer
- Digital logic/gates
- Telemetry and interface including I2C

With variants in each flavour

The 5x5 package base Silicon features:

- 8x MOSFETs with corresponding driver
- 2x200 mA programmable LDOs
- 2x200 mA preset LDOs (1.2 V, 3.3 V)
- OTP block
- On-board clock with 2 inbuilt oscillators
- 4 banks of analog/digital GPIOs
- SPI interface

AmP8DB6QF65
Power_Seminar_Demo

On-Board Clocks

On-board clock with 2 oscillators (500 kHz – 32 MHz)

- Divide into 8 clocks
- Capable of syncing with an outside clock using any GPIO
- Phase staggering

The screenshot shows the AnDAPT web interface with the 'Project Settings' dialog open. The dialog has three tabs: 'Phase', 'Platform BoM', and 'Verilog Library Versions'. The 'Phase' tab is active, showing configuration options for 'Clock A' and 'Clock B'. 'Clock A' is configured with a source of 4 MHz, an external name of 'ClockA', an external frequency of 1 MHz, and a mode of 7 Phases. 'Clock B' is configured with a source of 2 MHz, an external name of 'ClockB', an external frequency of 1 MHz, and a mode of Binary. Below these settings is a table of clock configurations:

Clock A		Clock B		Select	Name
A Freq	A Phase	B Freq	B Phase		
0.571 MHz	0°	0.008 MHz	0°	A	CK0
0.571 MHz	51°	0.016 MHz	0°	A	CK1
0.571 MHz	103°	0.031 MHz	0°	A	CK2
0.571 MHz	154°	0.063 MHz	0°	A	CK3
0.571 MHz	206°	0.125 MHz	0°	A	CK4
0.571 MHz	257°	0.25 MHz	0°	A	CK5
0.571 MHz	309°	0.5 MHz	0°	A	CK6
		1 MHz	0°	B	CK7

Below the table are dropdown menus for 'Oscillator A Select' (set to 'none') and 'Divide Clock A by' (set to 'none'). An 'Ok' button is at the bottom right of the dialog. The background shows a circuit board diagram with components like 'Power Block', 'Clock', and various GPIO pins.

Drag and Drop Library Components

The screenshot displays the AnDAPT WebApp interface for a project named "Power_Seminar_Demo" using the "AmP8DB6QF65" component. The interface is divided into several sections:

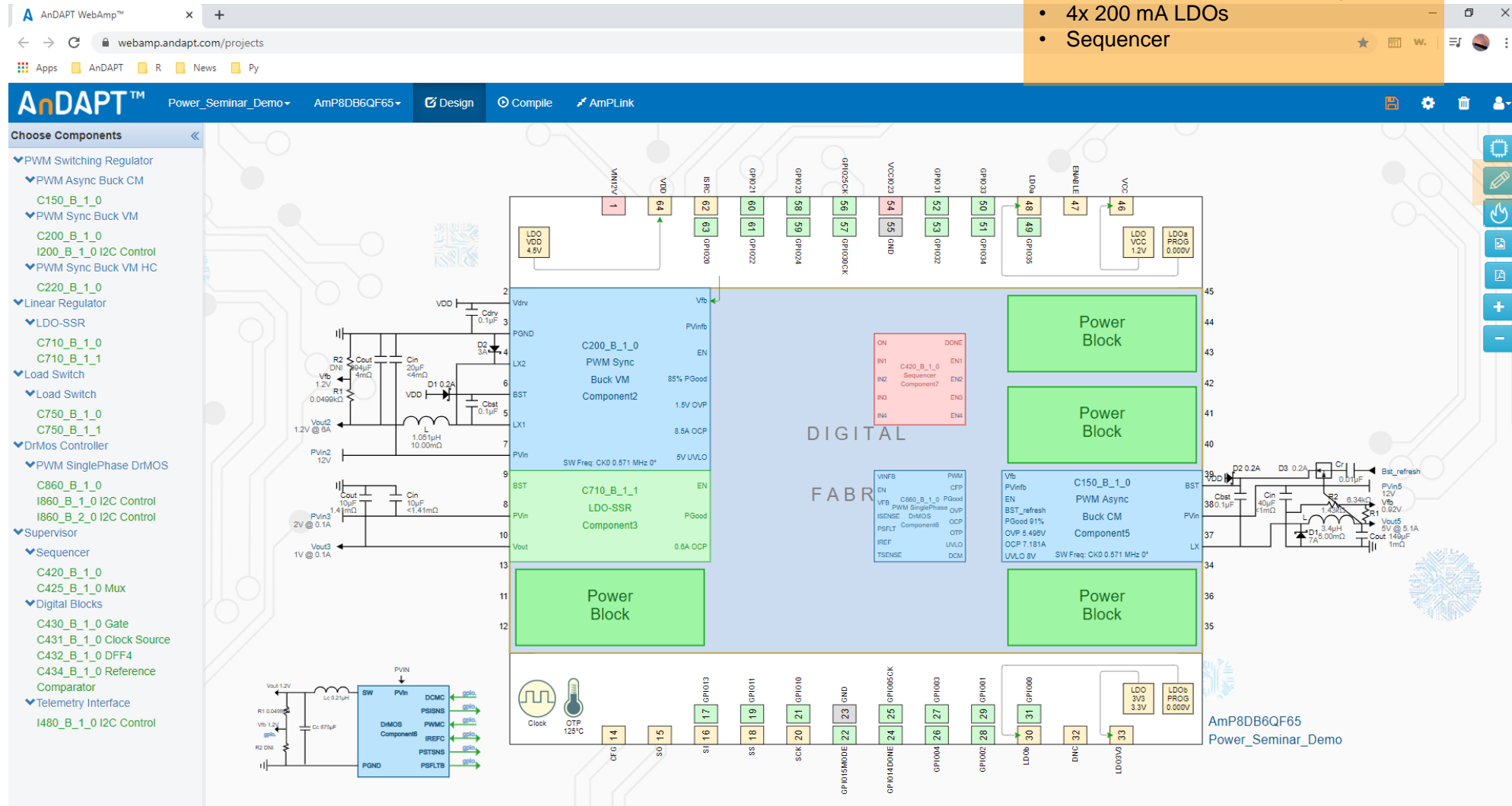
- Choose Components:** A sidebar on the left lists various component categories such as "PWM Switching Regulator", "Linear Regulator", "LDO-SSR", "Load Switch", "DrMOS Controller", "Supervisor", "Sequencer", "Digital Blocks", and "Telemetry Interface".
- IC Pinout:** The central area shows a detailed pinout diagram for the "AmP8DB6QF65" IC. The pins are arranged in a grid with labels like VDD, VDD1, VDD2, VDD3, VDD4, VDD5, VDD6, VDD7, VDD8, VDD9, VDD10, VDD11, VDD12, VDD13, VDD14, VDD15, VDD16, VDD17, VDD18, VDD19, VDD20, VDD21, VDD22, VDD23, VDD24, VDD25, VDD26, VDD27, VDD28, VDD29, VDD30, VDD31, VDD32, VDD33, VDD34, VDD35, VDD36, VDD37, VDD38, VDD39, VDD40, VDD41, VDD42, VDD43, VDD44, VDD45, VDD46, VDD47, VDD48, VDD49, VDD50, VDD51, VDD52, VDD53, VDD54, VDD55, VDD56, VDD57, VDD58, VDD59, VDD60, VDD61, VDD62, VDD63, VDD64, VDD65, VDD66, VDD67, VDD68, VDD69, VDD70, VDD71, VDD72, VDD73, VDD74, VDD75, VDD76, VDD77, VDD78, VDD79, VDD80, VDD81, VDD82, VDD83, VDD84, VDD85, VDD86, VDD87, VDD88, VDD89, VDD90, VDD91, VDD92, VDD93, VDD94, VDD95, VDD96, VDD97, VDD98, VDD99, VDD100.
- Component Placement:** Several "Power Block" components are placed on the IC. A blue box highlights a "Power Block" component being dragged from the "Choose Components" list to a specific pin on the IC. An orange callout box with an arrow points to this action, containing the text "Drag and drop components onto IC".
- IC Labels:** The IC is labeled "DIGITAL FABRIC" in the center. Other labels include "LDO VDD 4.5V", "LDO VDD 1.2V", "LDO VDD 3.3V", "LDO PRG 0.050V", "LDO PRG 0.050V", "Clock", "OTF 125°C", "GPG 14", "GPG 15", "GPG 16", "GPG 17", "GPG 18", "GPG 19", "GPG 20", "GPG 21", "GPG 22", "GPG 23", "GPG 24", "GPG 25", "GPG 26", "GPG 27", "GPG 28", "GPG 29", "GPG 30", "GPG 31", "GPG 32", "GPG 33", "GPG 34", "GPG 35", "GPG 36", "GPG 37", "GPG 38", "GPG 39", "GPG 40", "GPG 41", "GPG 42", "GPG 43", "GPG 44", "GPG 45", "GPG 46", "GPG 47", "GPG 48", "GPG 49", "GPG 50", "GPG 51", "GPG 52", "GPG 53", "GPG 54", "GPG 55", "GPG 56", "GPG 57", "GPG 58", "GPG 59", "GPG 60", "GPG 61", "GPG 62", "GPG 63", "GPG 64", "GPG 65", "GPG 66", "GPG 67", "GPG 68", "GPG 69", "GPG 70", "GPG 71", "GPG 72", "GPG 73", "GPG 74", "GPG 75", "GPG 76", "GPG 77", "GPG 78", "GPG 79", "GPG 80", "GPG 81", "GPG 82", "GPG 83", "GPG 84", "GPG 85", "GPG 86", "GPG 87", "GPG 88", "GPG 89", "GPG 90", "GPG 91", "GPG 92", "GPG 93", "GPG 94", "GPG 95", "GPG 96", "GPG 97", "GPG 98", "GPG 99", "GPG 100".

Integrate More Rails

Customize IC within minutes.

This example includes:

- 1x 6 A synchronous buck regulator
- 1x 2 A LDO
- 1x upto 40 A DrMOS controller
- 1x 6A asynchronous buck regulator
- 4x 200 mA LDOs
- Sequencer



Wire Connections and Naming Signals

The screenshot displays the AnDAPT web interface for a project named 'AmP8DB6QF65'. The interface is divided into several sections:

- Component Library (Left):** A list of components categorized by type, including PWM Switching Regulators, Linear Regulators, LDO-SSR, Load Switches, DrMOS Controllers, Supervisors, and Telemetry Interfaces.
- Schematic Diagram (Center):** A detailed circuit diagram showing a central 'DIGITAL FABR' block. It is connected to various power management components: a C200_B_1_0 PWM Async Buck CM, a C710_B_1_1 LDO-SSR, and a C150_B_1_0 PWM Async Buck CM. The diagram also shows several 'Power Block' components and a sequencer (C430_B_1_0). Various pins and nodes are labeled, such as VDD, VDD1, VDD2, VDD3, VDD4, VDD5, VDD6, VDD7, VDD8, VDD9, VDD10, VDD11, VDD12, VDD13, VDD14, VDD15, VDD16, VDD17, VDD18, VDD19, VDD20, VDD21, VDD22, VDD23, VDD24, VDD25, VDD26, VDD27, VDD28, VDD29, VDD30, VDD31, VDD32, VDD33, VDD34, VDD35, VDD36, VDD37, VDD38, VDD39, VDD40, VDD41, VDD42, VDD43, VDD44, VDD45, VDD46, VDD47, VDD48, VDD49, VDD50, VDD51, VDD52, VDD53, VDD54, VDD55, VDD56, VDD57, VDD58, VDD59, VDD60, VDD61, VDD62, VDD63, VDD64, VDD65, VDD66, VDD67, VDD68, VDD69, VDD70, VDD71, VDD72, VDD73, VDD74, VDD75, VDD76, VDD77, VDD78, VDD79, VDD80, VDD81, VDD82, VDD83, VDD84, VDD85, VDD86, VDD87, VDD88, VDD89, VDD90, VDD91, VDD92, VDD93, VDD94, VDD95, VDD96, VDD97, VDD98, VDD99, VDD100.
- Callout Box (Right):** A yellow box with a blue arrow pointing to the schematic, containing the text:
 - Connect nodes by simply dragging wires using mouse
 - Rename signals and wires

Power Component Options

The screenshot displays the AnDAPT web interface for a power design project. The browser address bar shows `webamp1.andapt.com/project/5248`. The interface includes a top navigation bar with 'Design', 'Compile', and 'AmPLink' buttons. On the left, a 'Choose Components' sidebar lists various power components such as 'PWM Switching Regulator', 'LDO', and 'Supervisor'. The main workspace shows a schematic diagram of the AmP8DB6QF65 power management IC. A right-click context menu is open over a 'Power Block' component, listing options: 'Rename', 'Tune', 'Highlight Wires', 'Hide Wires', 'Delete', and 'Version'. An orange callout box points to the 'Version' option with the text: 'Right click power components for further options including latest component version'. The schematic includes various power rails (VDD, VDD12V, VDD1.2V, VDD0.000V) and components like capacitors, inductors, and diodes. The bottom right corner of the schematic area shows the device name 'AmP8DB6QF65' and the project name 'Power_Seminar_Demo'.

Synchronous Buck Details

Basic Configuration

- SW Freq: CK0 0.571 MHz
- PVIN Voltage: 12 V
- PVin Name: PVin2
- Output Voltage: 1.2 V
- Vout Name: Vout2
- Vout Ripple: 0.09 %
- Vout Overshoot: 0.01 V
- Output Current: 6 A
- Iout Ripple: 31.52 %
- Iout Delta: 3 A

LC Components

- Manual Set LC
- Inductor: 1 μ H
- Inductor DCR: 11.6 m Ω
- Capacitor: 394 μ F
- Cap ESR: 4 m Ω
- f_{Lc}: 8.0 kHz

Vfb Resistor Components

- Manual Set Resistors
- R1: 0.0499 k Ω
- R2: DNI k Ω

Synchronous buck regulator configuration parameters include:

- Switching frequency, phase
- Input/Output voltage
- Output voltage ripple/overshoot
- Current
- Inductor/capacitor value and part number selection
- Fault settings including UVLO/OCP/OVP/OTP
- Power Good
- Startup slew rate
- ... and more

Schematic Diagram:

- Inputs: >Vfb, >PVinfb, >EN, <PGood 85%, <OVP 1.5V, <OCP 8.5A, <UVLO 5V
- Outputs: Vout2 1.2V @ 6A, PVin2 12V
- Internal Nodes: Vdrv, PGND, LX2, BST, LX1, PVin
- Components: Cdrv 0.1 μ F, D2 3A, Cin 20 μ F <4m Ω , Cout 394 μ F 4m Ω , R2 DNI, R1 0.0499k Ω , Vfb 1.2V, D1 0.2A, Cbst 0.1 μ F, L 1 μ H 11.60m Ω , VDD
- Model: C200_B_1_0, PWM Sync, Buck VM, Component12
- SW Freq: CK0 0.571 MHz 0°

Bill of Materials

Inbuilt BoM tool enables component selection within GUI

Basic Configuration

SW Freq: CK0 0.571 MHz
PVin Voltage: 12 V
PVin Name: PVin2
Output Voltage: 1.2 V
Vout Name: Vout2
Vout Ripple: 0.09 %
Vout Overshoot: 0.01 V
Output Current: 6 A
Iout Ripple: 31.52 %
Iout Delta: 3 A

LC Components

Manual Set LC

Inductor: 1 μ H
Inductor DCR: 11.6 m Ω
Capacitor: 394 μ F
Cap ESR: 4 m Ω
 f_{LC} : 8.0 kHz

Vfb Resistor Components

Manual Set Resistors

R1: 0.0499 k Ω
R2: DNI k Ω

Update Reset Done

Part	Description	Recommended Attributes	Attributes	Inductor AC Loss (W)	Quantity	Part Number	Spec	Manufacturer	Select
C200_B_1_0	PWM Sync Buck VM		Vout2, 1.2V @ 6A		1	V200420			
L	Inductor	1.051 μ H, 10m Ω , >6A	1 μ H, 11.6m Ω , >6A	0.0502	1	78438357010		Wurth Elektronik	<button>Wurth</button>
Cout	Capacitor	375 μ F, 4m Ω , >1.2V	394 μ F, 4m Ω , >1.2V		1				<button>Wurth</button>
Cin	Capacitor	20 μ F, <4m Ω , >12V	20 μ F, <4m Ω , >12V		1				<button>Wurth</button>
Cbst	Capacitor	0.1 μ F, >6V	0.1 μ F, >6V		1				<button>Wurth</button>
Cdrv	Capacitor	0.1 μ F, >6V	0.1 μ F, >6V		1				<button>Wurth</button>
D1	Schottky Diode	200mA, 0.5V	200mA, 0.5V		1	RB521S30T5G		ON Semiconductor	<button>DigiKey</button>
D2	Schottky Diode	3A, 0.54V	3A, 0.54V		1	DB2W40300L		Panasonic Electronic Components	<button>DigiKey</button>
R1	Resistor	0.0499k Ω , 1%, 0.063W	0.0499k Ω , 1%, 0.063W		1				<button>DigiKey</button>
R2	Resistor	DNI	DNI		0				

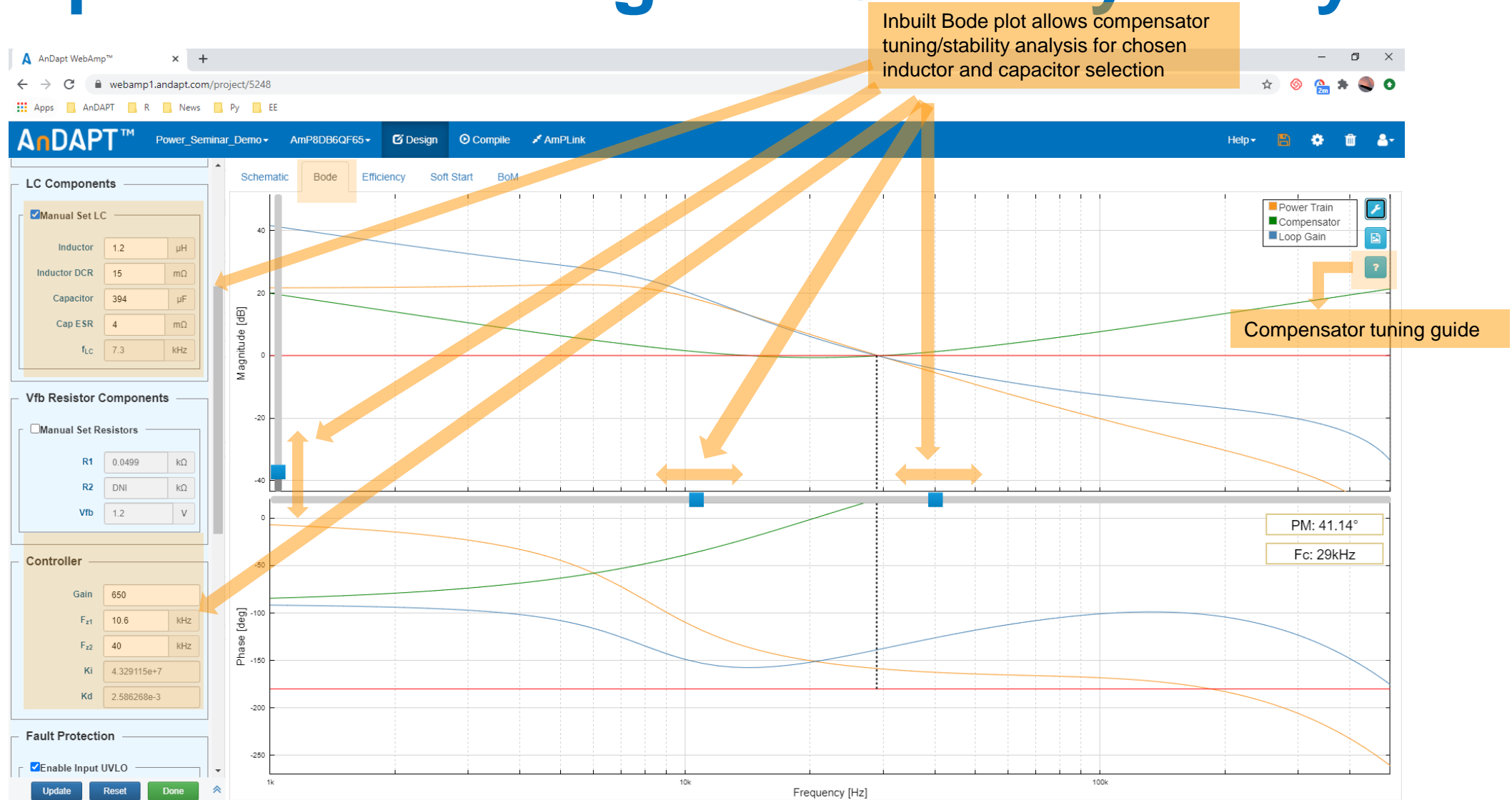
Explore Detailed Component Parameters

Inductor selection provides data from Würth RedExpert for wide range of suitable inductors

The screenshot shows the AnDAPT web application interface. A 'Select L Inductor' dialog box is open, displaying a table of inductor parameters. The table includes columns for Spec, L (μH), R_{DC,typ} (mΩ), I_a (A), I_{sat} (A), Size, Length (mm), Width (mm), Height (mm), Series, AC_Loss, Code, Order, and Manufacturer. The background interface shows the 'Basic Configuration' section with SW Freq, PVin Voltage, Output Voltage, and Vout Name. The 'LC Components' section includes Manual Set LC with Inductor, Inductor DCR, Capacitor, Cap ESR, and f_c. The 'Vfb Resistor Components' section includes Manual Set Resistors with R1 and R2.

Spec	L (μH)	R _{DC,typ} (mΩ)	I _a (A)	I _{sat} (A)	Size	Length (mm)	Width (mm)	Height (mm)	Series	AC_Loss	Code	Order	Manufacturer
<input type="checkbox"/>	1.00	12.00	7.20	9.00	4020	4.10	4.10	2.00	WE-MAPI	0.05	74438356010	Free Sample	Würth Elektronik
<input type="checkbox"/>	1.20	15.00	5.80	9.00	4020	4.10	4.10	2.00	WE-MAPI	0.04	74438356012	Free Sample	Würth Elektronik
<input type="checkbox"/>	1.50	16.00	5.80	7.80	4020	4.10	4.10	2.00	WE-MAPI	0.03	74438356015	Free Sample	Würth Elektronik
<input type="checkbox"/>	1.00	11.60	7.40	9.60	4030	4.10	4.10	3.10	WE-MAPI	0.05	74438357010	Free Sample	Würth Elektronik
<input type="checkbox"/>	1.20	13.40	7.00	8.80	4030	4.10	4.10	3.10	WE-MAPI	0.05	74438357012	Free Sample	Würth Elektronik
<input type="checkbox"/>	1.50	17.10	6.20	8.50	4030	4.10	4.10	3.10	WE-MAPI	0.04	74438357015	Free Sample	Würth Elektronik
<input type="checkbox"/>	1.00	13.50	7.20	6.50	4020	4.10	4.10	2.00	WE-MAPI	0.05	74438356010HT	Free Sample	Würth Elektronik
<input type="checkbox"/>	1.20	16.00	5.80	6.90	4020	4.10	4.10	2.00	WE-MAPI	0.04	74438356012HT	Free Sample	Würth Elektronik
<input type="checkbox"/>	1.50	18.00	5.50	6.60	4020	4.10	4.10	2.00	WE-MAPI	0.03	74438356015HT	Free Sample	Würth Elektronik
<input type="checkbox"/>	1.00	11.40	7.80	10.00	5020	5.40	5.40	2.10	WE-MAPI	0.04	74438366010	Free Sample	Würth Elektronik
<input type="checkbox"/>	1.50	18.50	6.70	9.50	5020	5.40	5.40	2.10	WE-MAPI	0.03	74438366015	Free Sample	Würth Elektronik
<input type="checkbox"/>	1.00	10.00	9.00	9.60	5030	5.40	5.40	3.10	WE-MAPI	0.04	74438367010	Free Sample	Würth Elektronik
<input type="checkbox"/>	1.00	10.00	4.80	4.60	6823	6.80	6.80	2.30	WE-TPC	0.11	744062001	Free Sample	Würth Elektronik
<input type="checkbox"/>	1.00	4.90	8.00	9.50	1028	10.00	10.00	2.80	WE-TPC	0.09	744065001	Free Sample	Würth Elektronik
<input type="checkbox"/>	1.50	7.30	7.20	7.50	1028	10.00	10.00	2.80	WE-TPC	0.06	7440650015	Free Sample	Würth Elektronik
<input type="checkbox"/>	1.50	5.20	7.20	10.00	1038	10.00	10.00	3.80	WE-TPC	0.04	7440660015	Free Sample	Würth Elektronik
<input type="checkbox"/>	1.00	9.00	5.37	6.40	7332	7.30	7.30	3.20	WE-PD	0.10	744778001	Free Sample	Würth Elektronik
<input type="checkbox"/>	1.00	8.40	6.84	10.20	7345	7.30	7.30	4.30	WE-PD	0.07	744777001	Free Sample	Würth Elektronik
<input type="checkbox"/>	1.50	13.70	6.20	9.50	1030	10.00	10.00	3.00	WE-PD	0.07	7447713015	Free Sample	Würth Elektronik

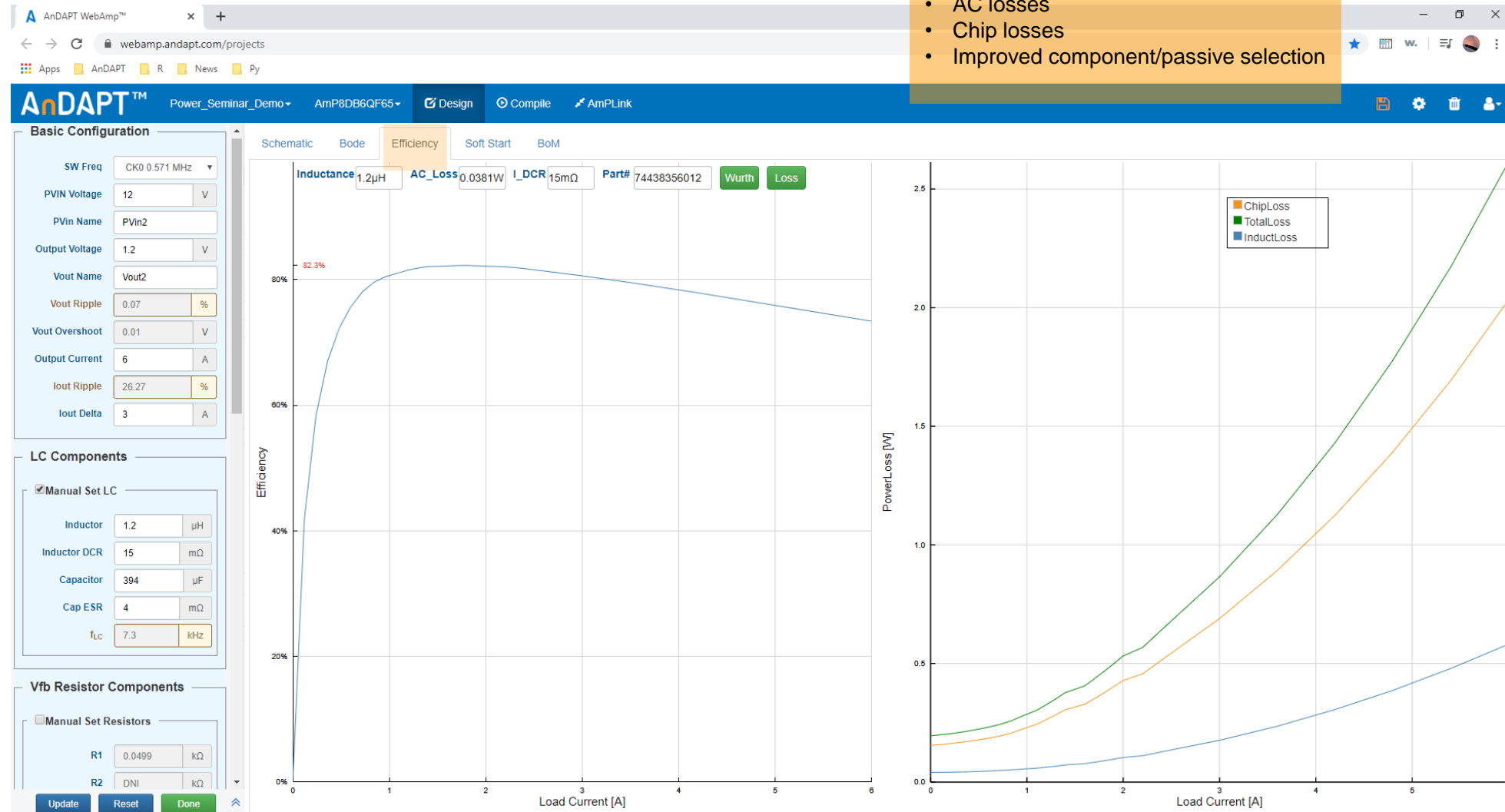
Compensator Tuning and Stability Analysis



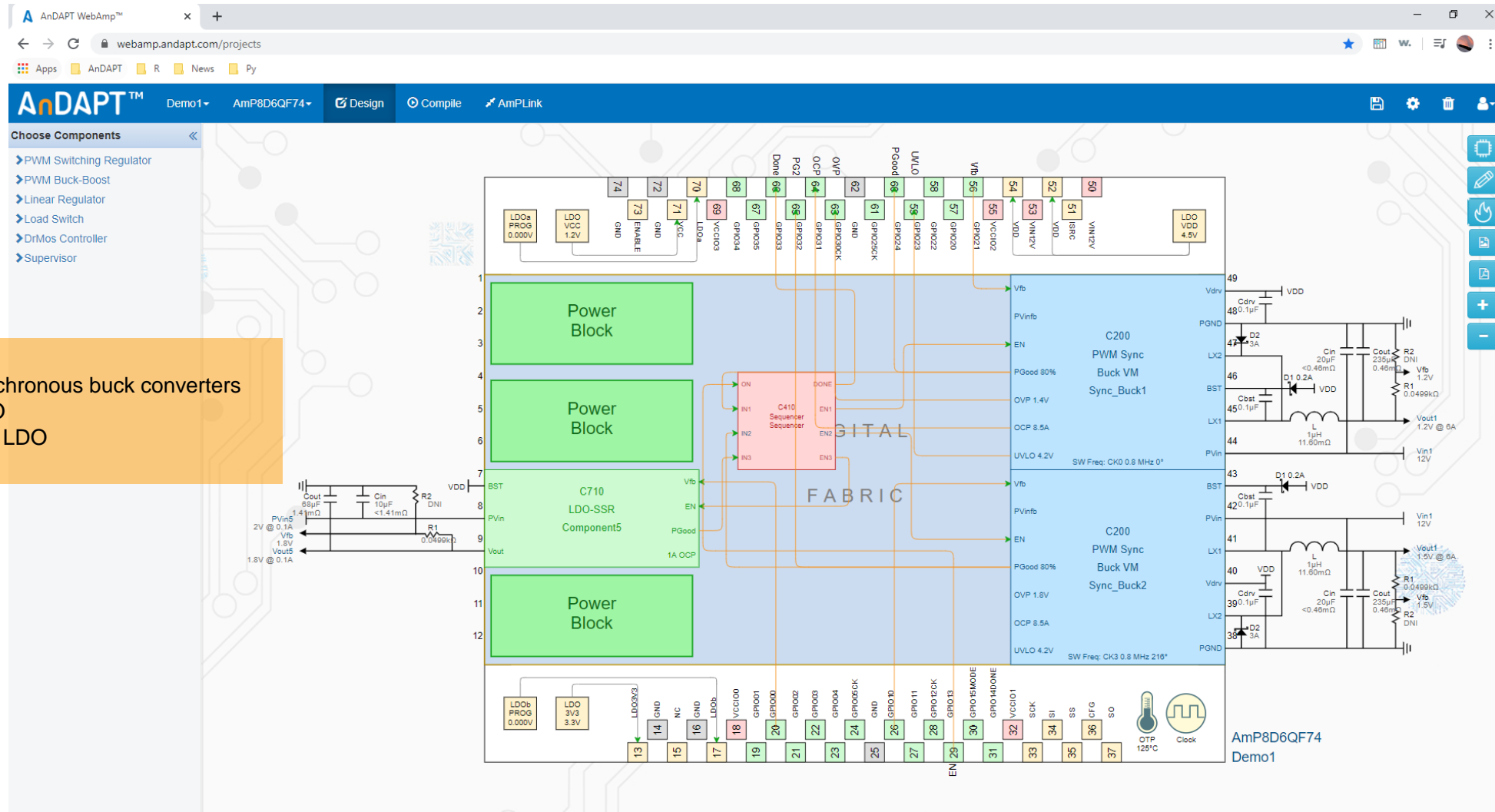
Efficiency Determination

Inbuilt efficiency plots features:

- Eval board PCB parasitics
- DC losses
- AC losses
- Chip losses
- Improved component/passive selection



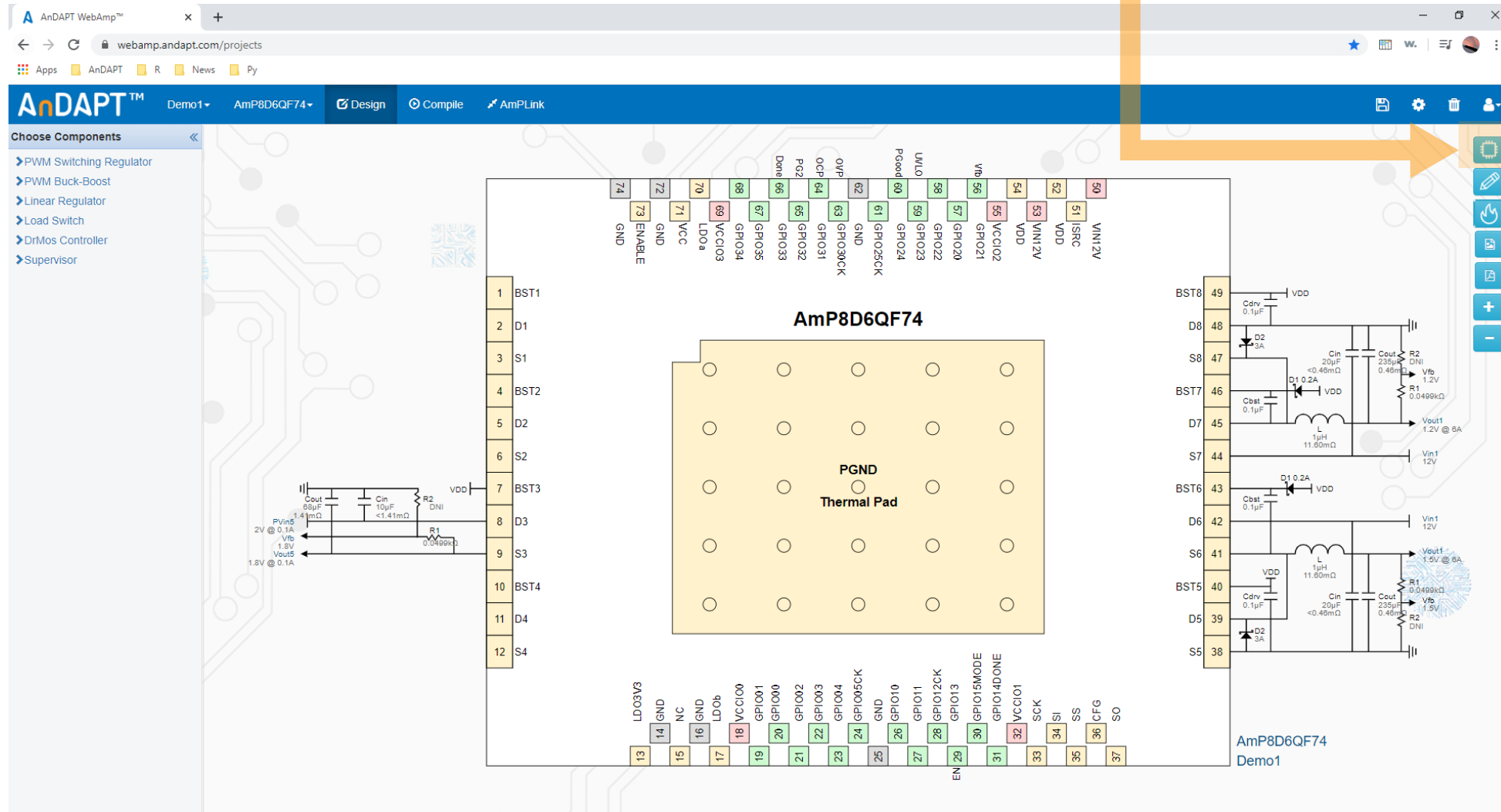
Demo Example (8x8 Package IC)



IC includes:

- 2x 6 A synchronous buck converters
- 1x 2 A LDO
- 4x 200 mA LDO
- Sequencer

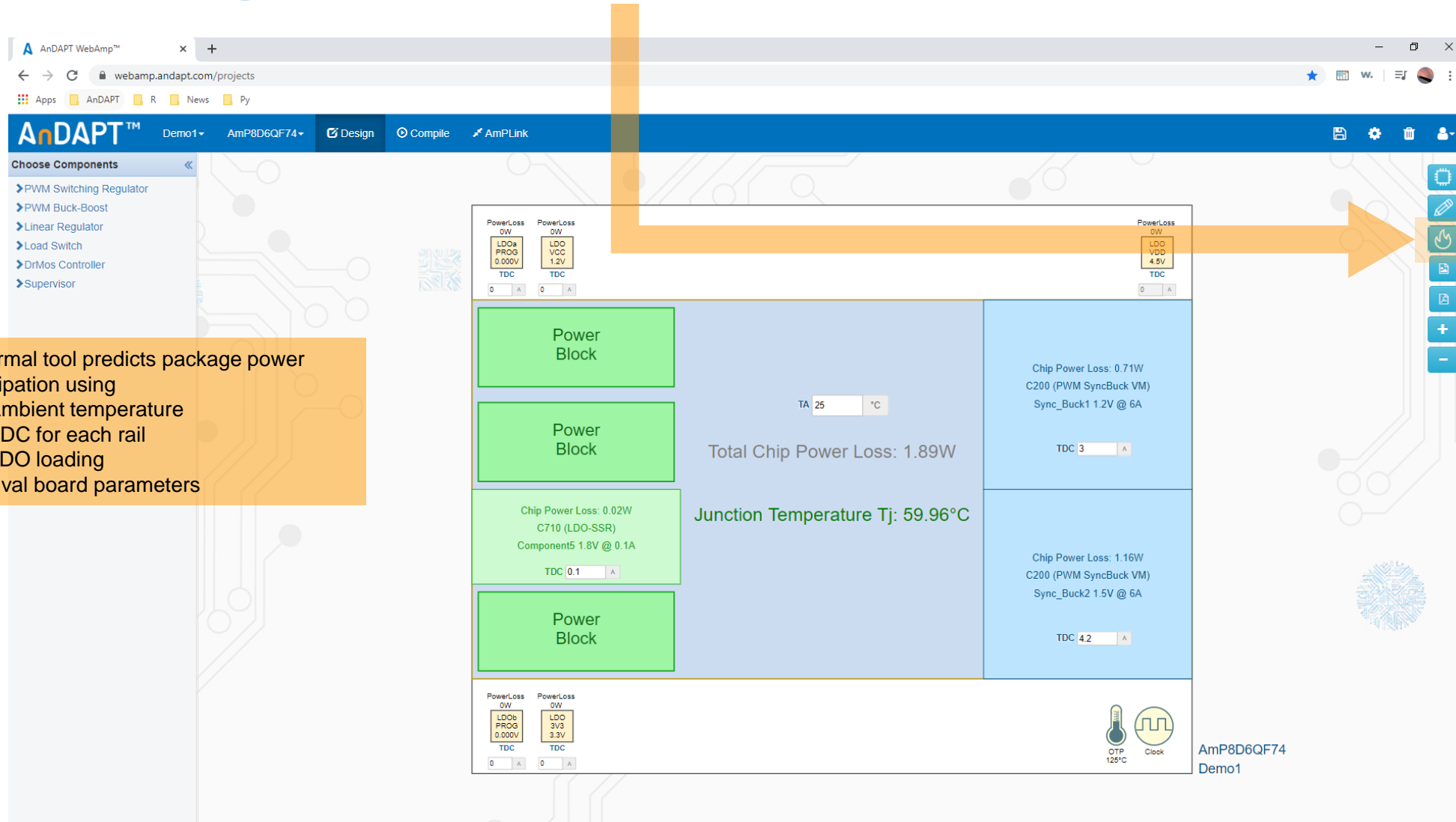
IC Pinout View for Schematic Design



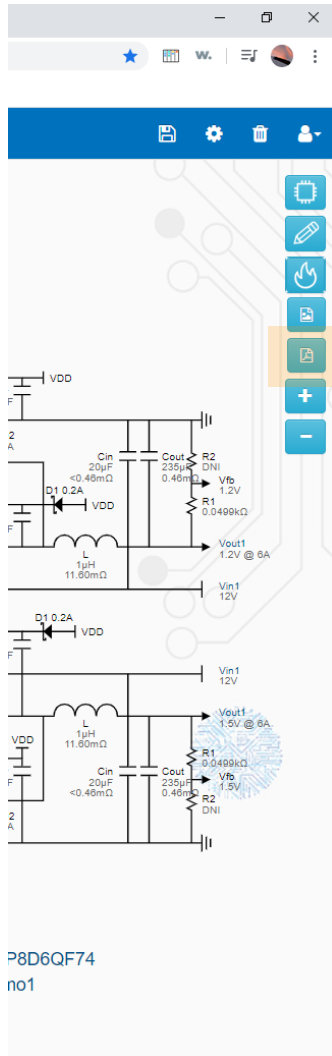
IC Package Thermal View

Thermal tool predicts package power dissipation using

- Ambient temperature
- TDC for each rail
- LDO loading
- Eval board parameters



Auto Custom Datasheet Generation



AmPMIC_Demo1.pdf 1 / 19

AmPMIC: Demo1

Features

- AmPMIC enables programmable custom PMIC
- Integrate application targeted Power Components
- Power Blocks for a variety of topologies
 - Scalable Integrated N-channel MOSFETs (SIM)
 - Current sense for protection, telemetry, regulation
 - Build Switching topologies - Buck, Boost, Buck-Boost
 - Build Linear topologies - LDO, Load Switch
 - Build Mixed topologies - Battery Charger
 - Build BLDC topologies - H-Bridge
- Sensor Blocks, sensing voltages and currents
 - Regulation, protection and telemetry
 - Adaptive Digitizer (ADi)
 - Threshold Comparators (ThC)
- Summation Amplifier (SuM)
- Noise-immune Reference (Nref) Array
- Analog fabric connectivity for sensor signals
- Digital μ Logic fabric connectivity: Analog/Digital Blocks
 - Analog and Digital GPIOs, LUTs for logic & Interface
 - Integrated Compensator RAM (CRAM)
 - On-demand POL control loops and interfaces
 - Precision Modulation Timer Array
 - Industry first: Analog Proficiency - Digital Flexibility

Order Information

Part Number	Package	Body Size
AmP8D6QF74	QF74	8x8

Power Component Summary

Category	Function	Part	Series	Name	Key Parameter
PWM Switching Regulator	PWM Sync Buck VM	C200S	Pro	Sync_Buck1	1.2V@6A
PWM Switching Regulator	PWM Sync Buck VM	C200S	Pro	Sync_Buck2	1.5V@6A
Linear Regulator	LDO-SSR	C710S	Pro	Component5	1.8V@0.1A
Supervisor	Sequencer	C410	Pro	Sequencer	

Power Component View

Design Compilation and File Generation

The screenshot displays the AnDAPT web interface for project compilation. The top navigation bar includes 'Demo1', 'AmP8D6QF74', 'Design', 'Compile', and 'AmPLink'. A 'Compile' button is visible on the left. The main content area is divided into two sections: 'Tools' and 'Downloadable Files'.

Tools	Result	Log File
Logic Synthesis	Success	View Log
Placement & Route	Success	View Log
Bit Generation	Success	View Log

Compile Log

```
Number of AnD_PTG_OSC 1
Number of AnD_DFFN 25
Number of AnD_DFF 56
Number of LUT4 280
```

Resource Usage...

io	10 used (Capacity 24)
clb	35 used (Capacity 64)
cm	5 used (Capacity 8)
pmt	7 used (Capacity 16)
sim	5 used (Capacity 8)
atc	3 used (Capacity 6)
corner	4 used (Capacity 4)
ptg	2 used (Capacity 2)
uLogic	280 used (Capacity 512)

Components Stats...

Stechmaplotp_fuse_module	
AnD_DFF	3
AnD_DFFN	7

Downloadable Files

Project Configuration	View	Download
Impl Netlist	View	Download
Synthesis Script	View	Download
Pin Constraint	View	Download
Bit Stream Hex (Flash program) CheckSum = 0x75d9	View	Download CheckSum
Bit Stream Hax (AmP Platform configuration)	View	Download
Bill of Materials	View	Download

- IC design compilation provides:
- Intel Hex file to pre-program flash
 - Hax file to directly program the IC
 - BoM file
 - IC resource utilization data

Additional Information

- Website: andapt.com
- Register: andapt.com/register
- Datasheets: andapt.com/docs
- Demoboards: andapt.com/software-tools
- Evaluation Board Video Tutorials
 - PMIC with Quad Bucks [AnD8400EB](#)
 - PMIC with DrMOS, Dual Bucks and LDOs [AnD7220EB](#)
- About AnDAPT: [AnDAPT_Backgrounder](#)
- Contact Sales: <mailto:sales@andapt.com>

